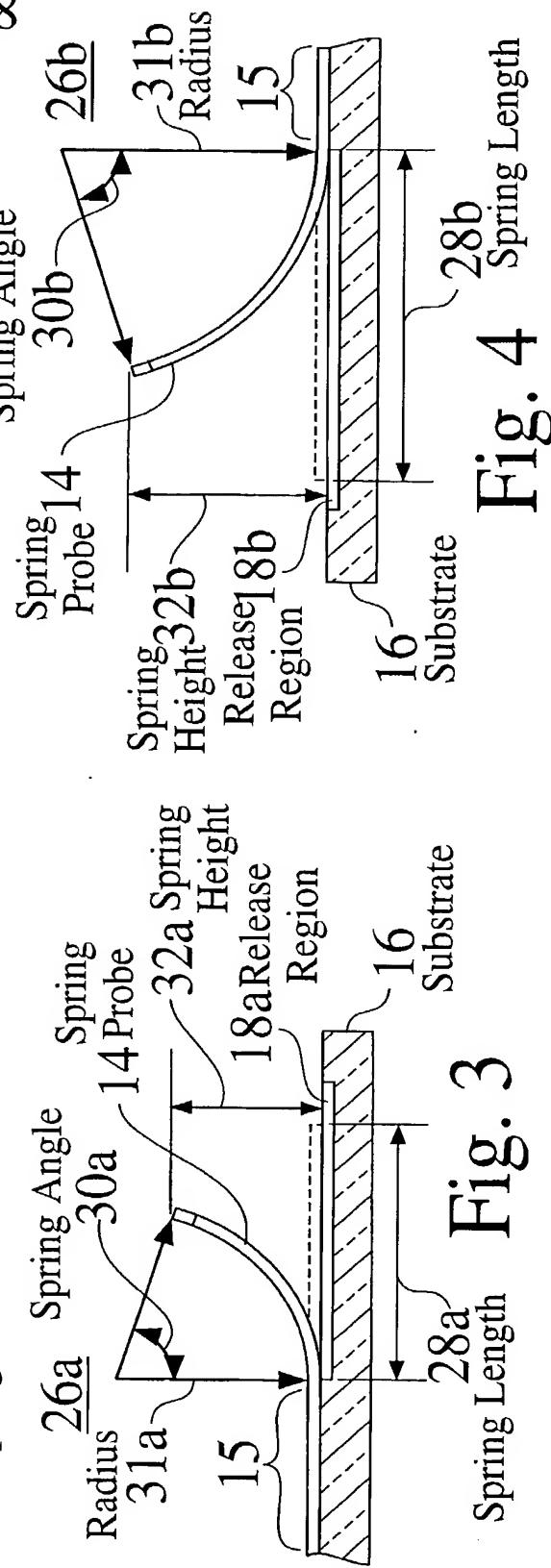


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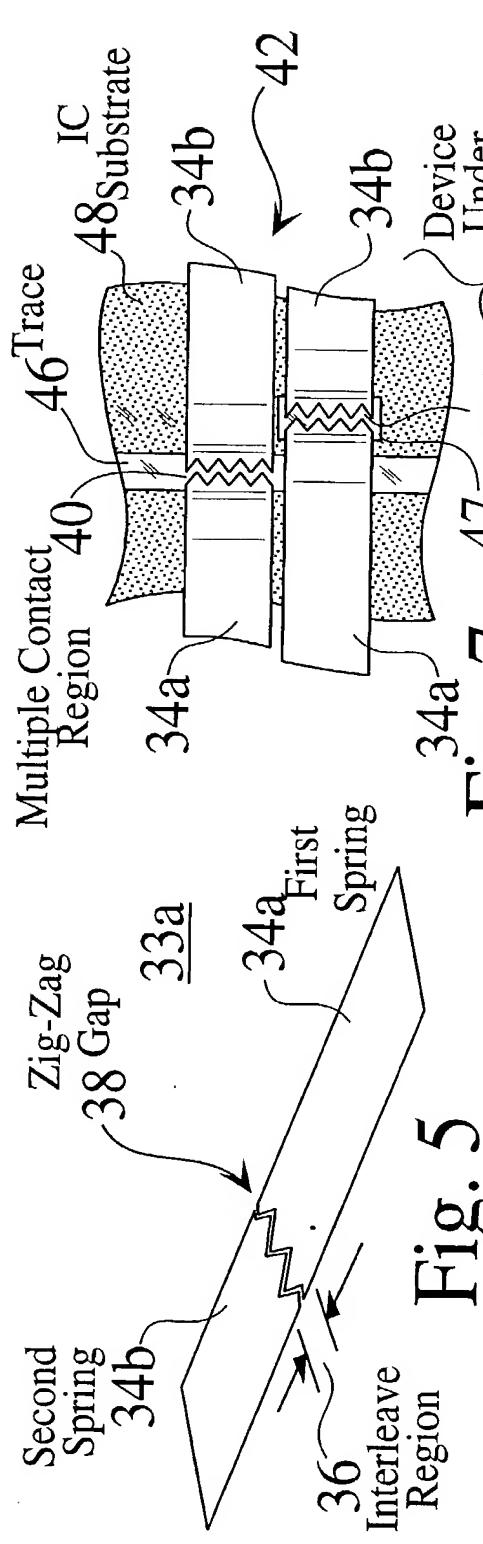
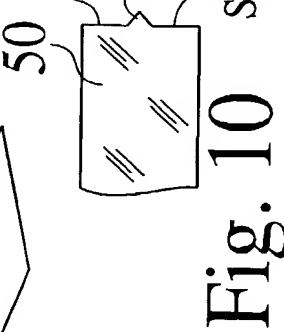
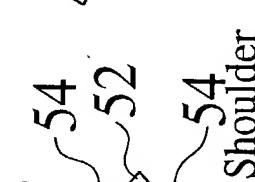
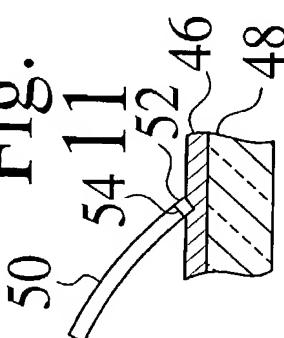
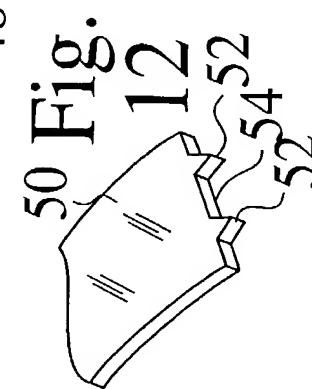
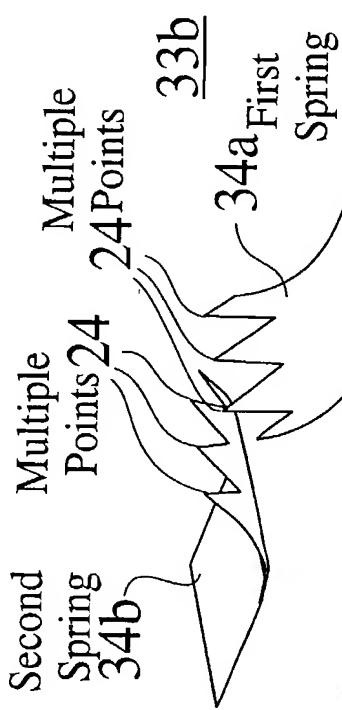
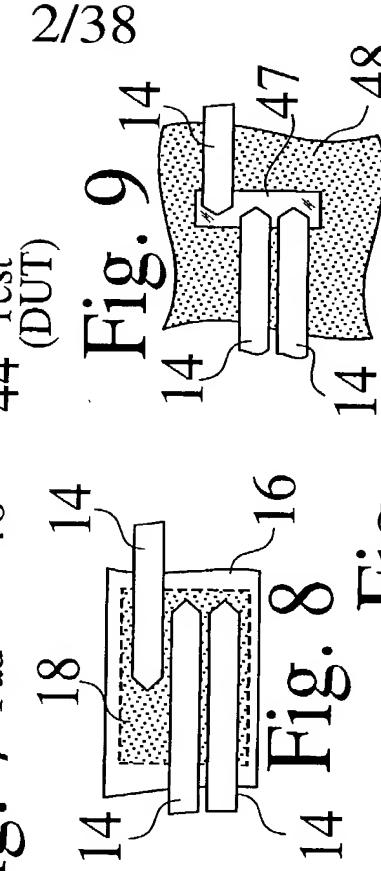


Fig. 7 Pad 47 40 44 40  
Device Under Test (DUT)



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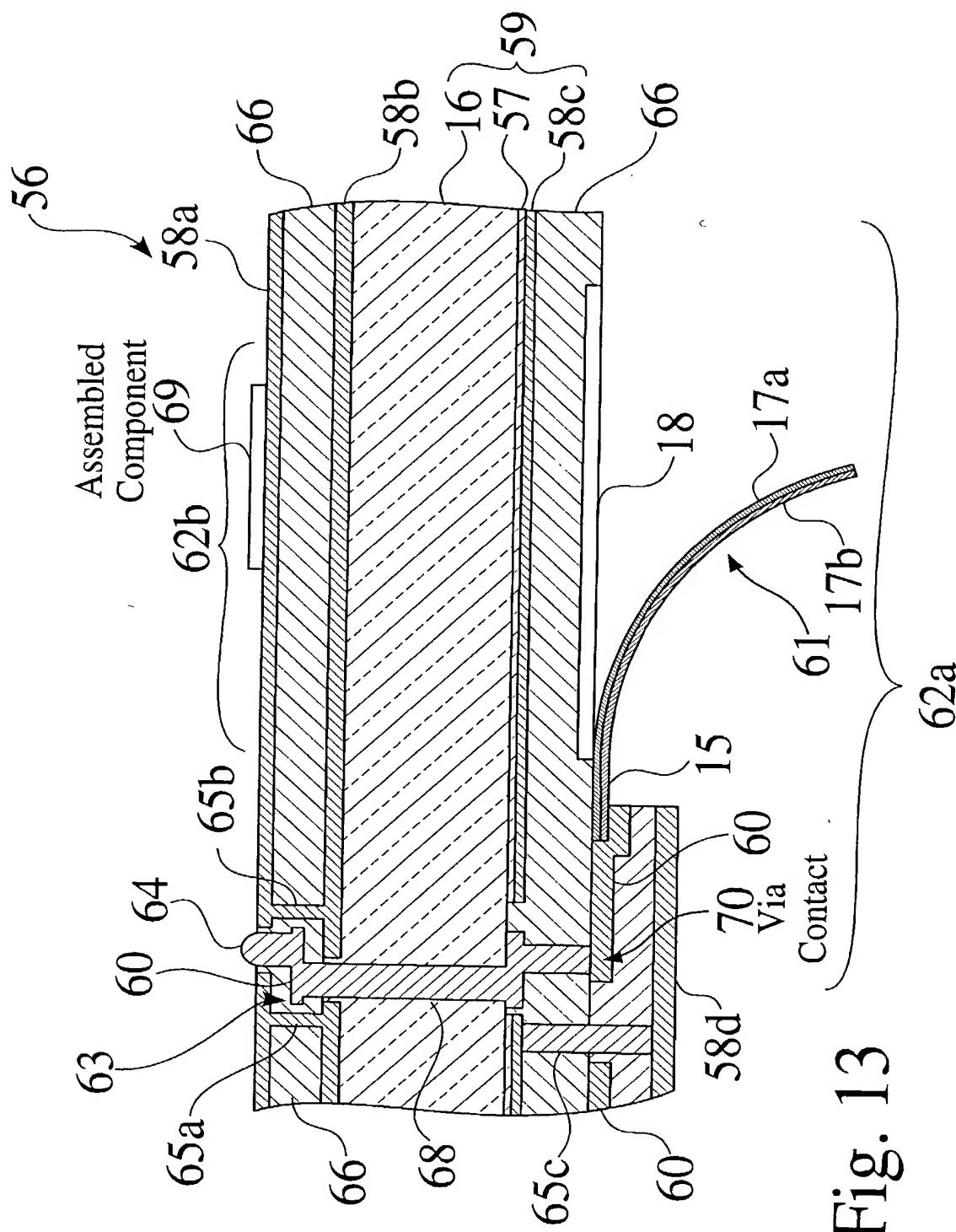


Fig. 13

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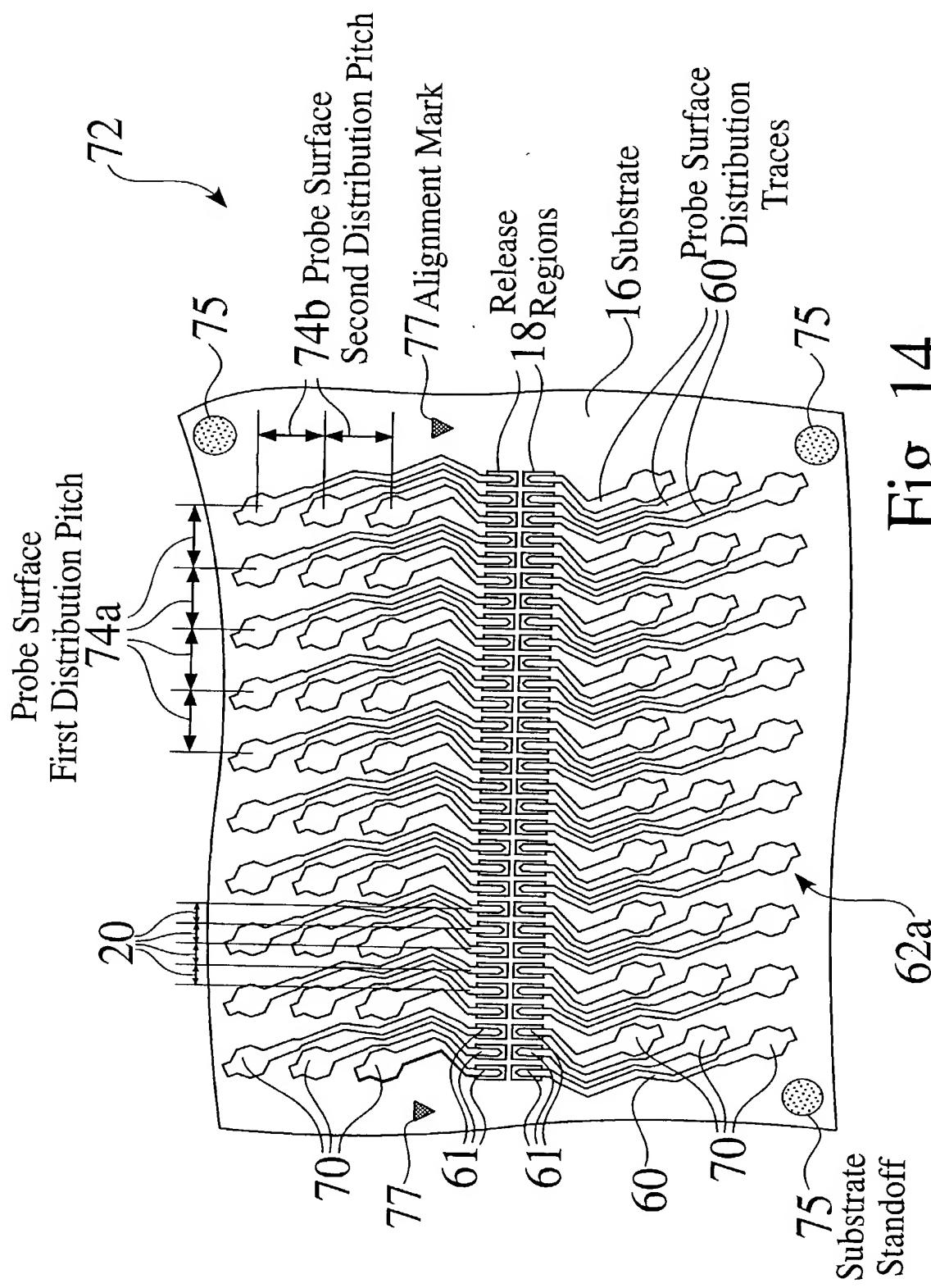


Fig. 14

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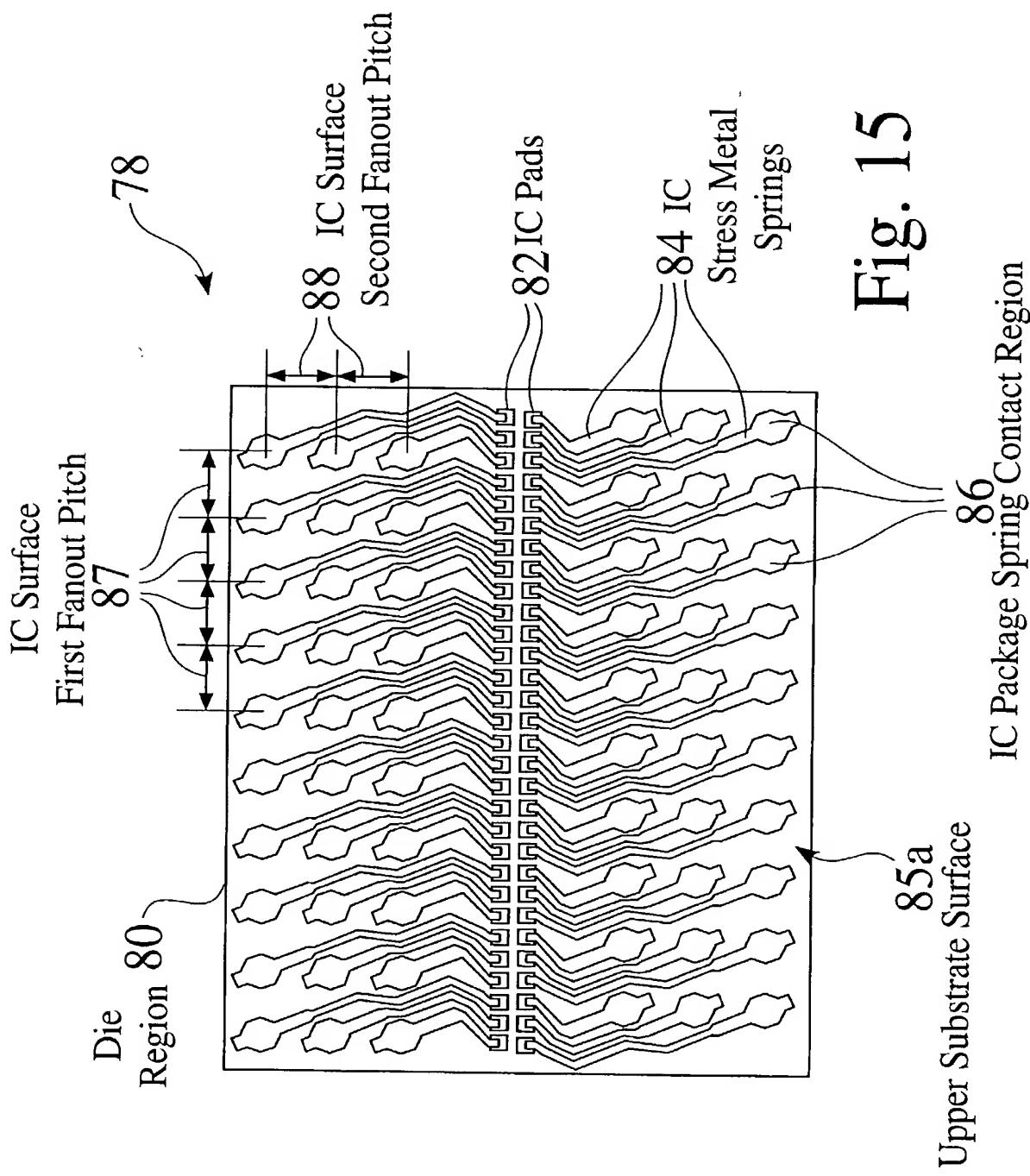


Fig. 15

IC Package Spring Contact Region

Upper Substrate Surface 85a

86

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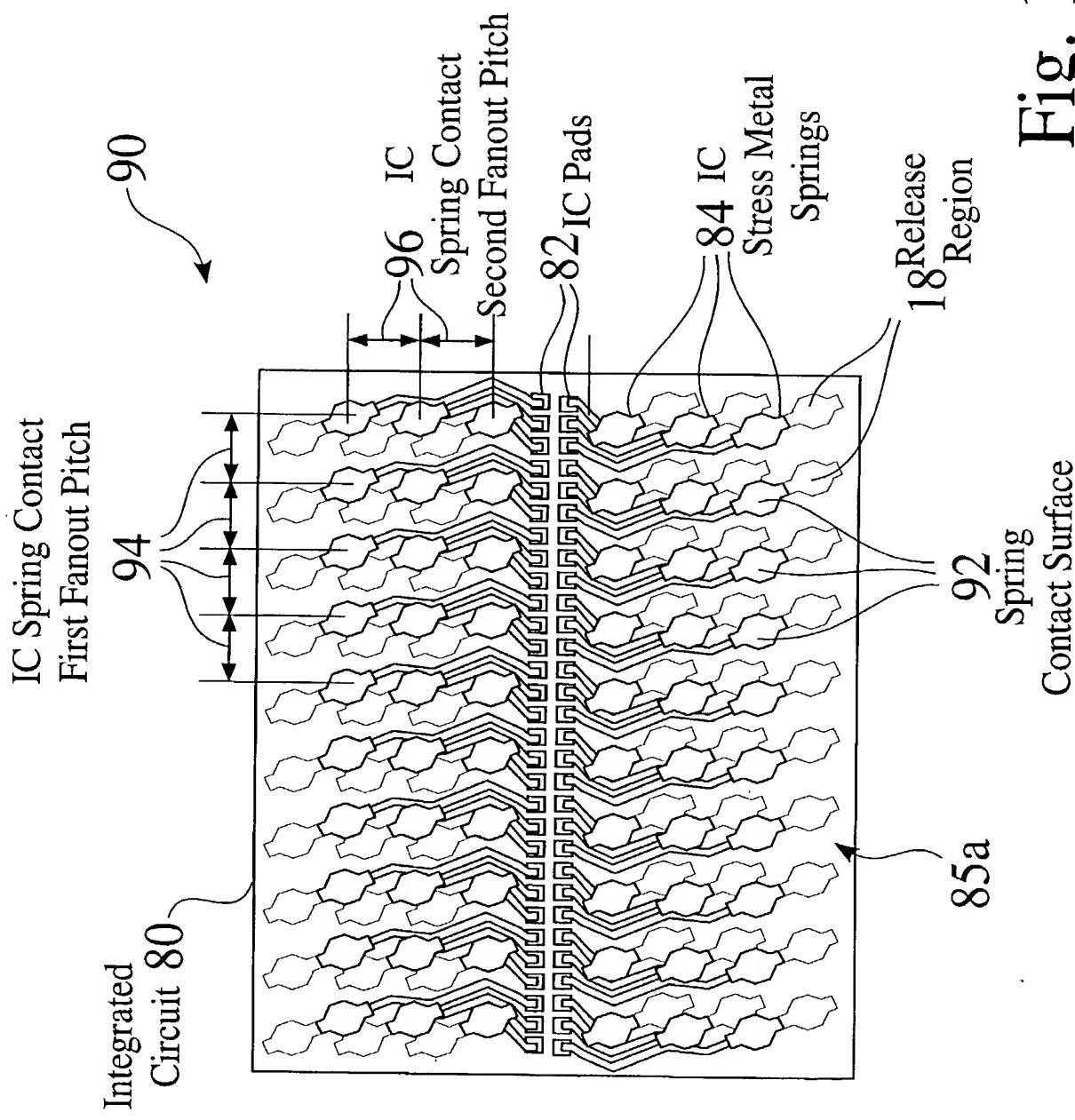


Fig. 16

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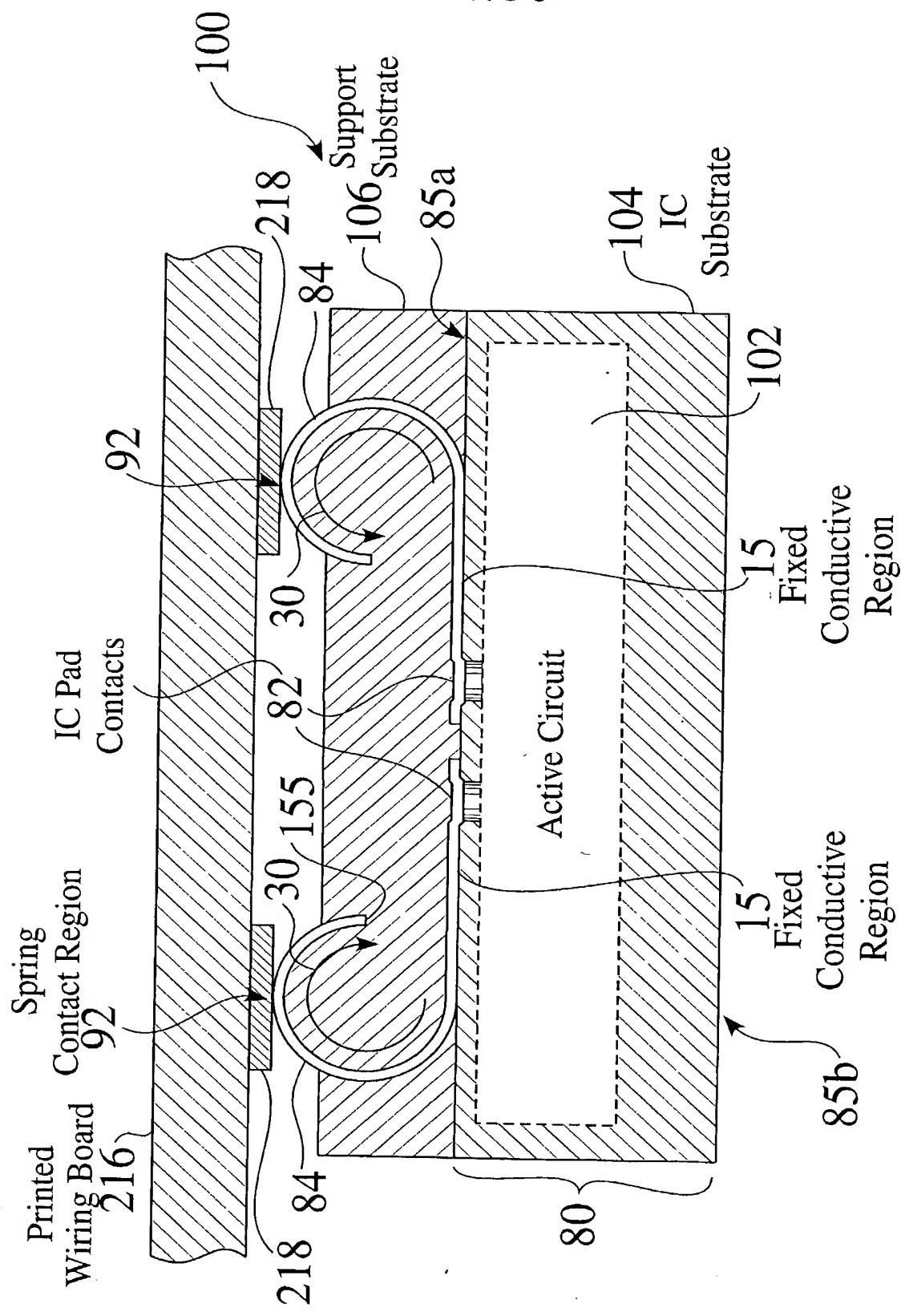
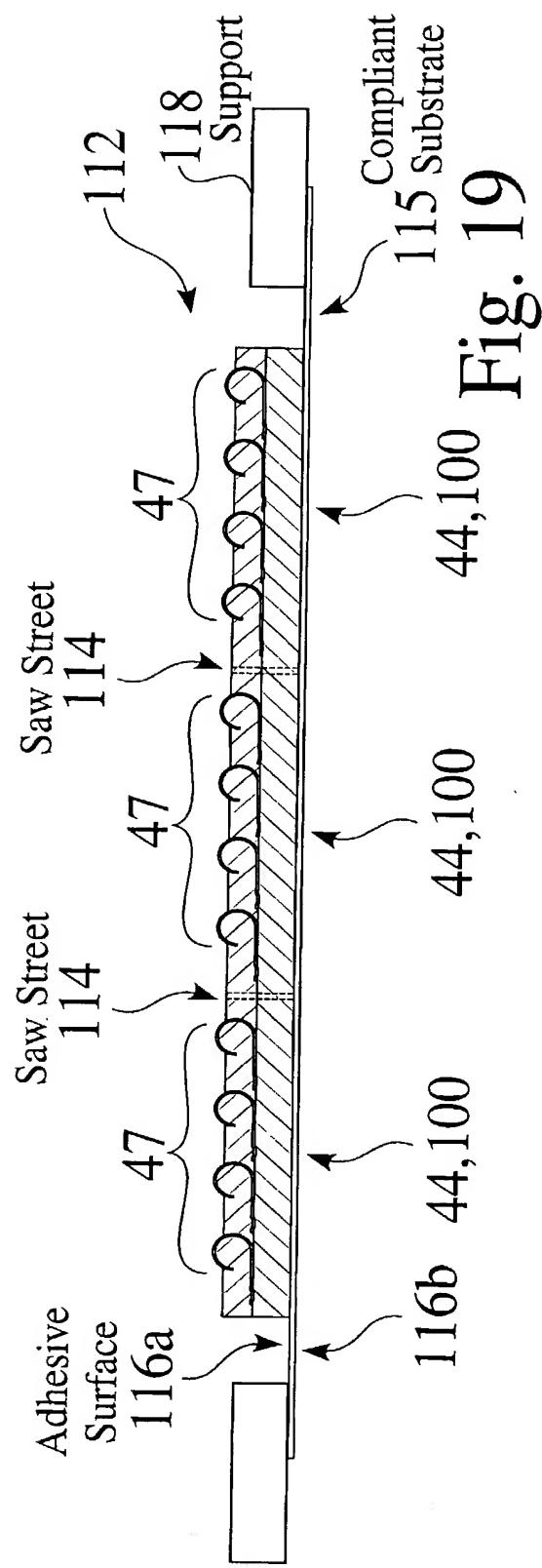
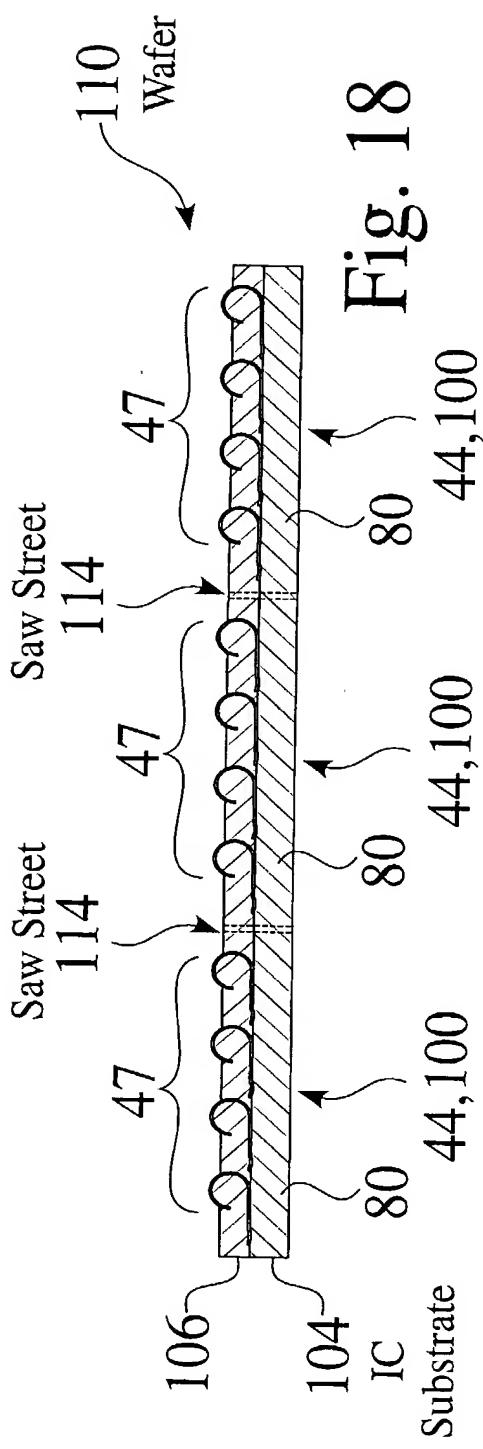
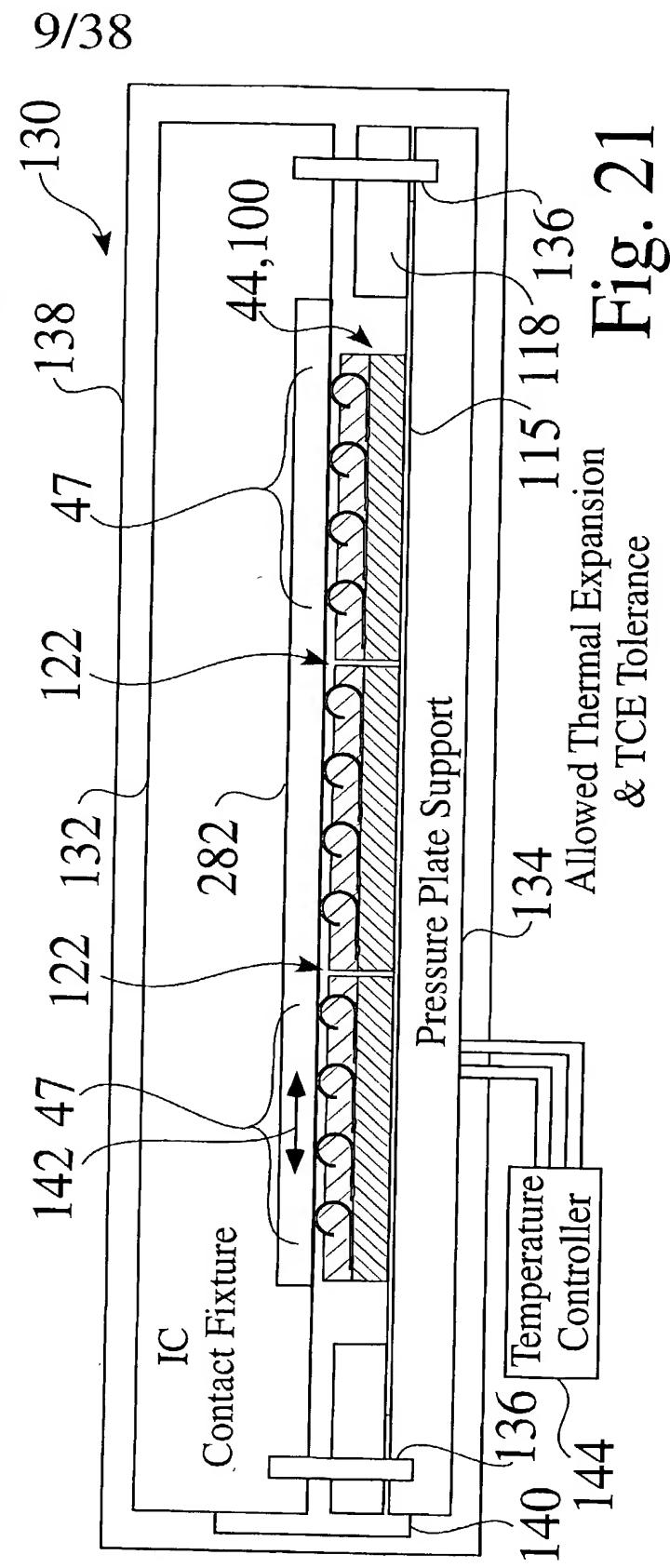
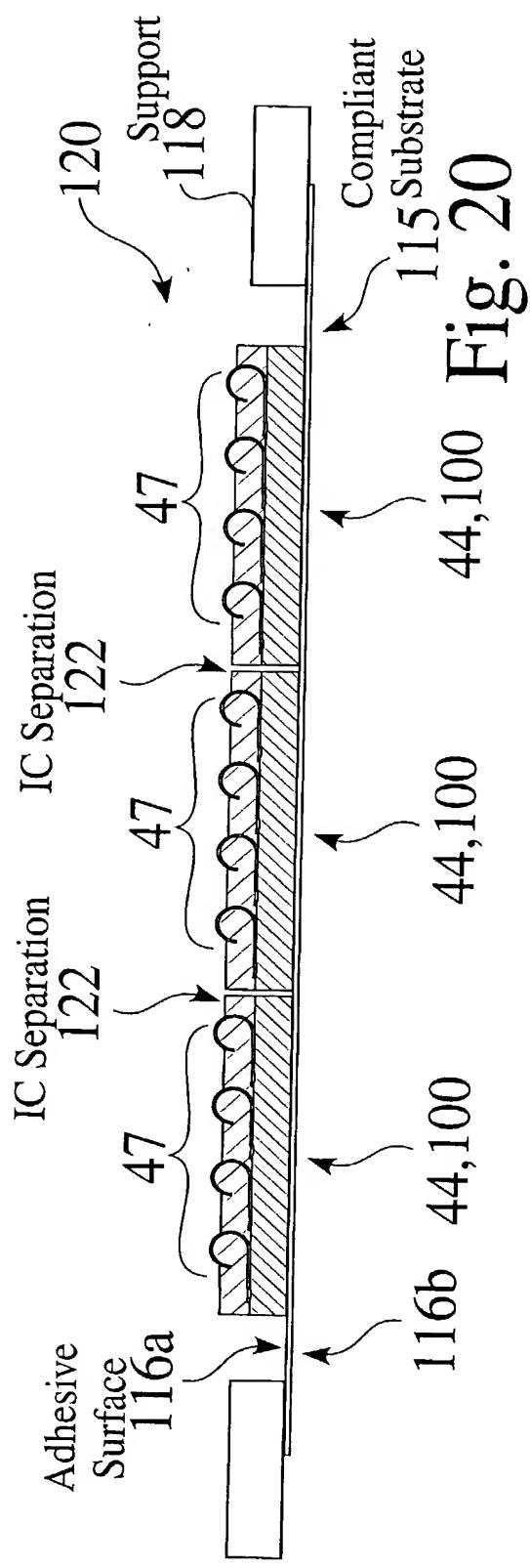


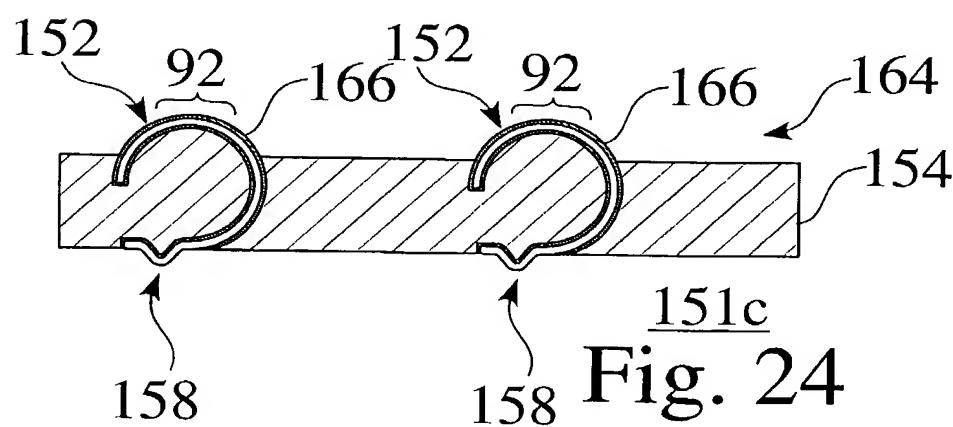
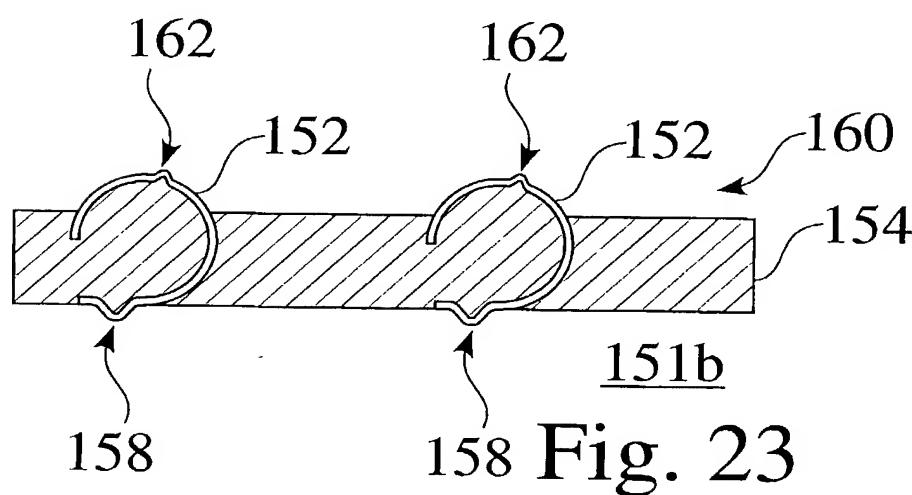
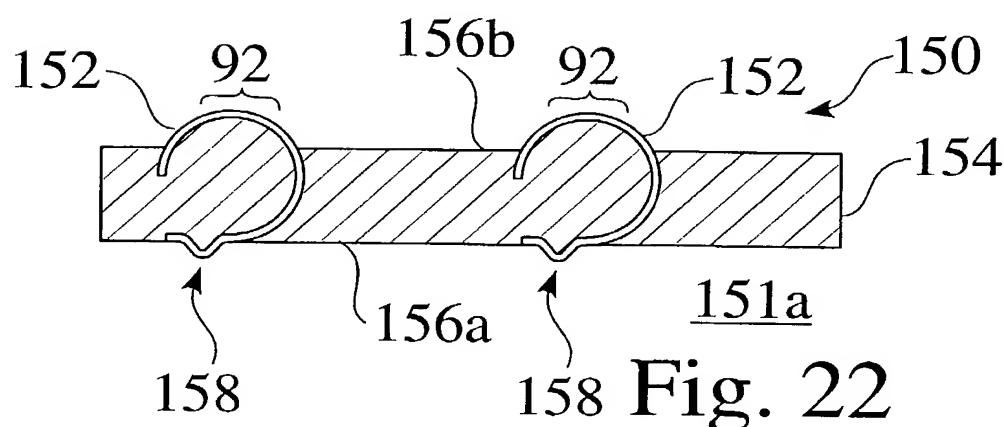
Fig. 17

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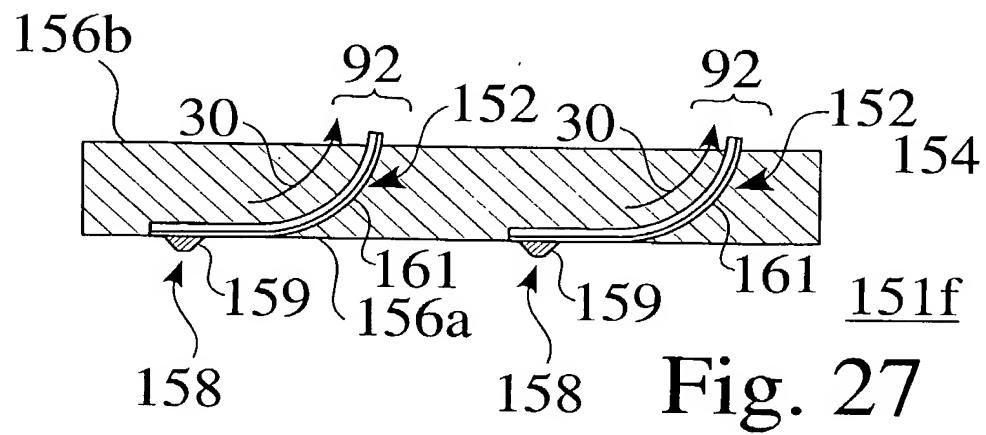
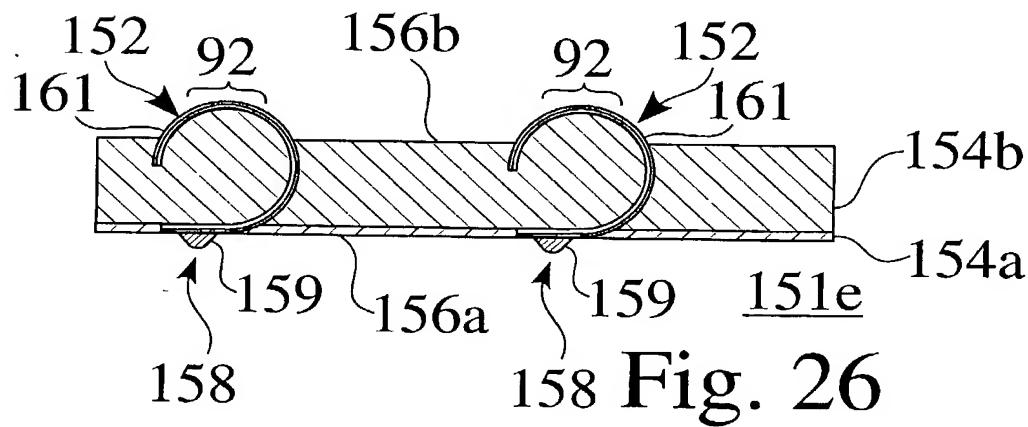
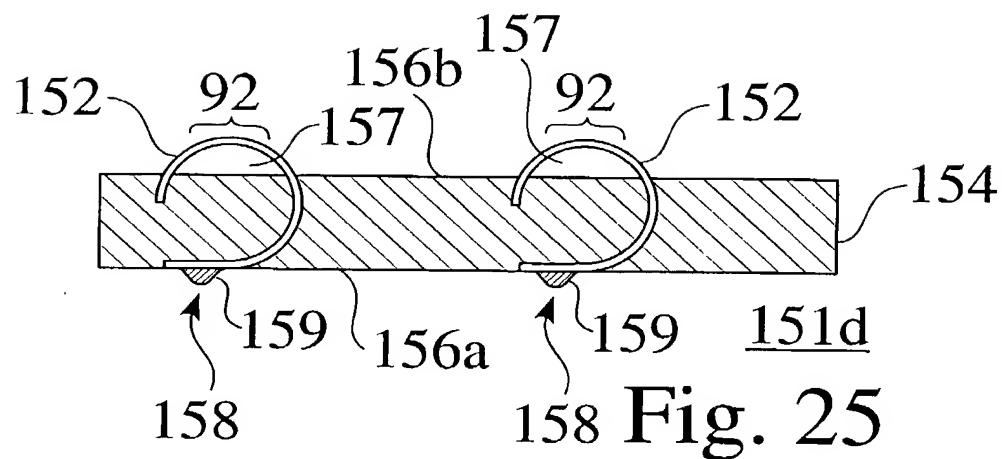




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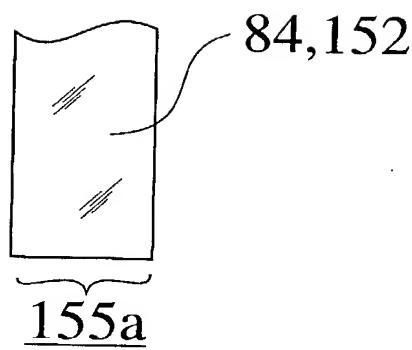


Fig. 28

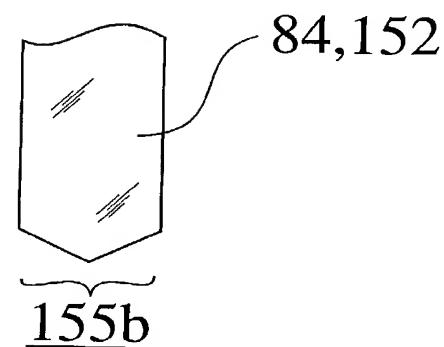


Fig. 29

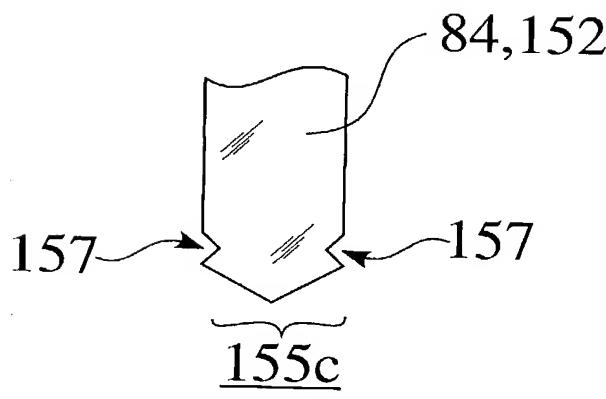


Fig. 30

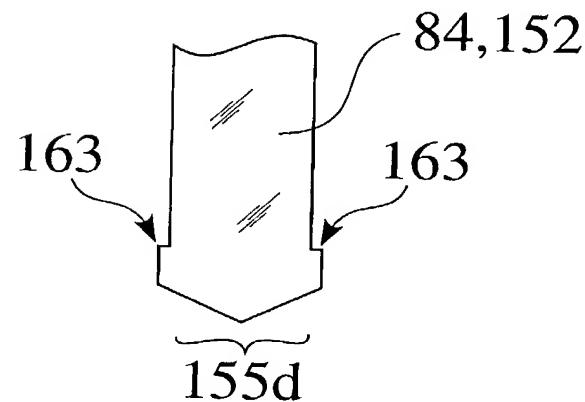
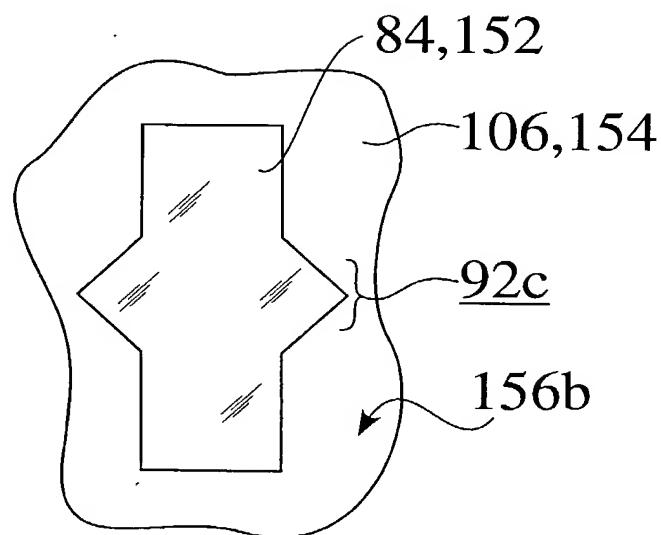
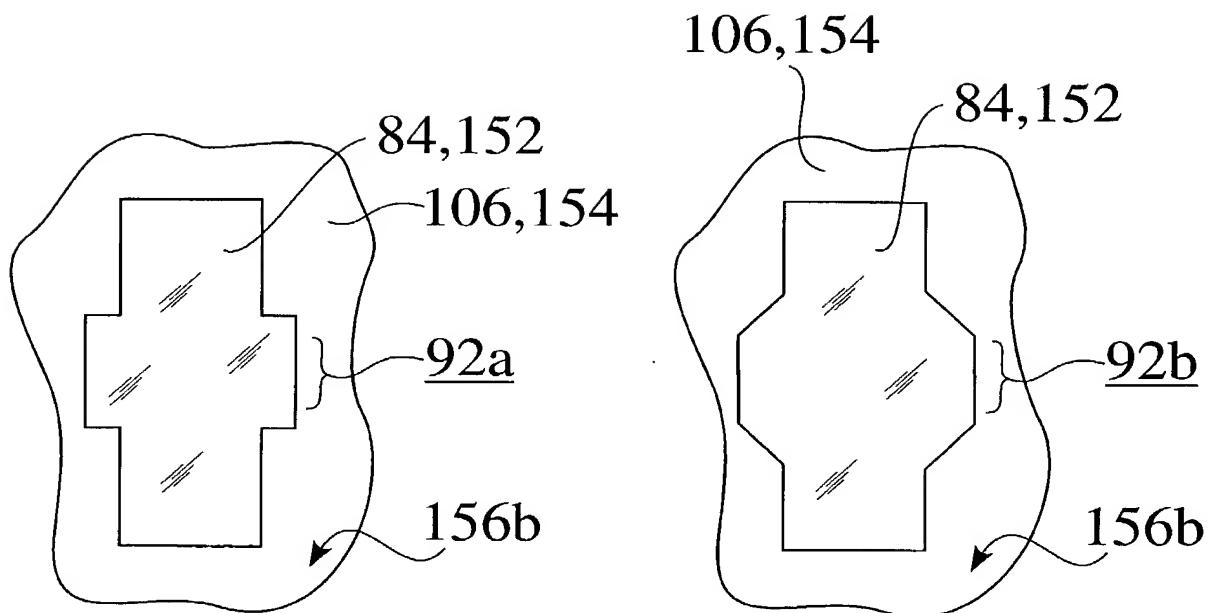
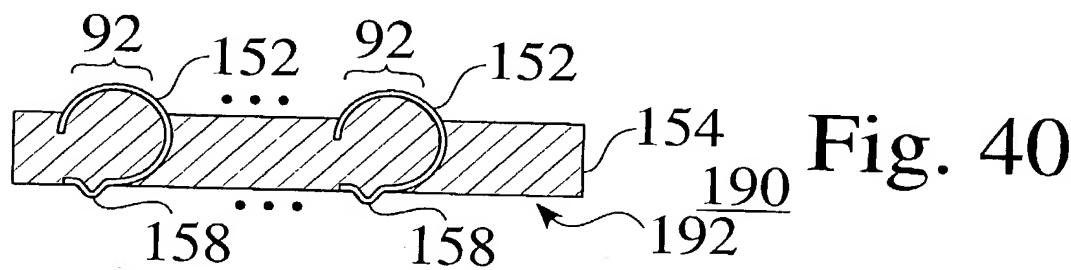
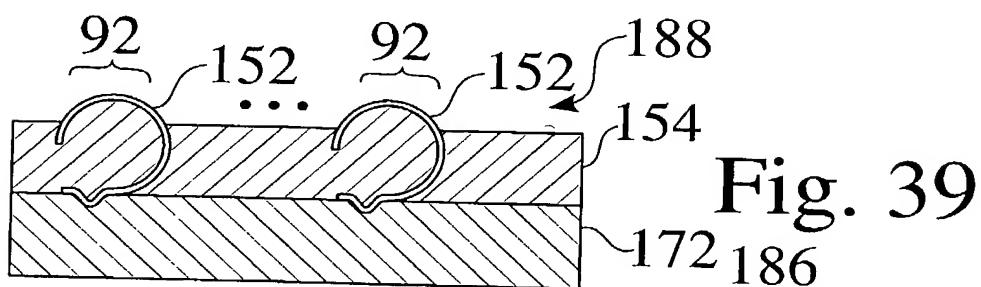
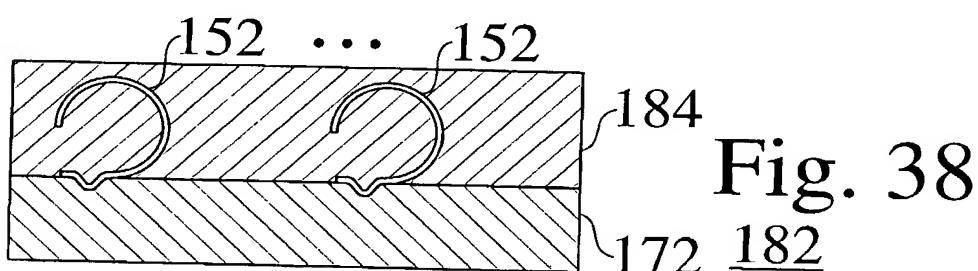
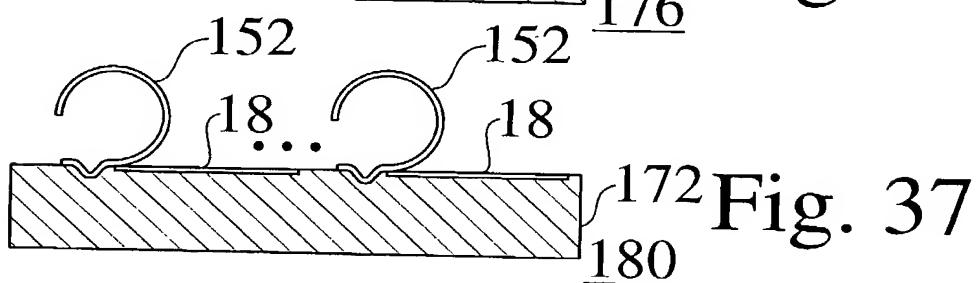
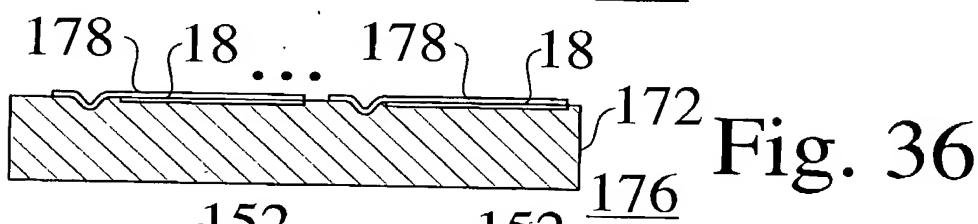
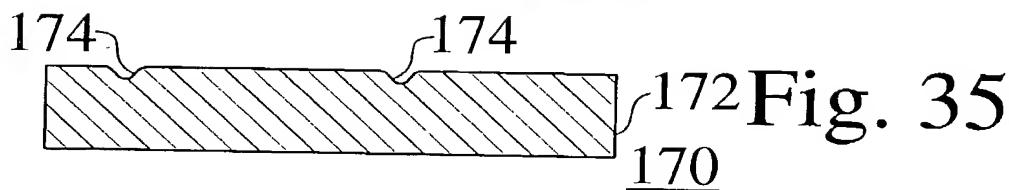


Fig. 31

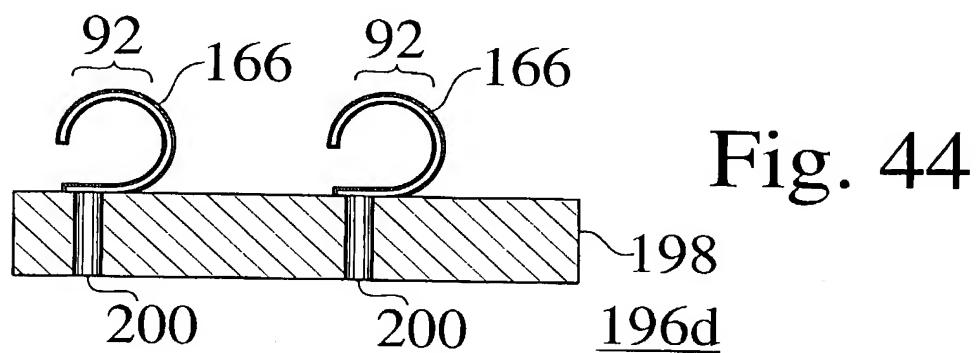
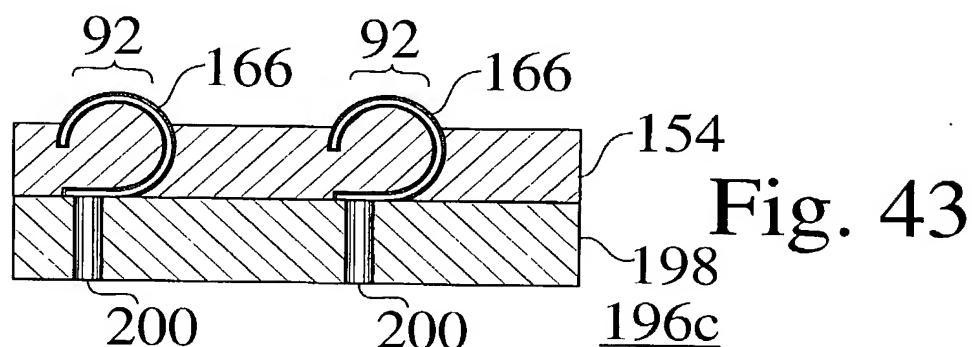
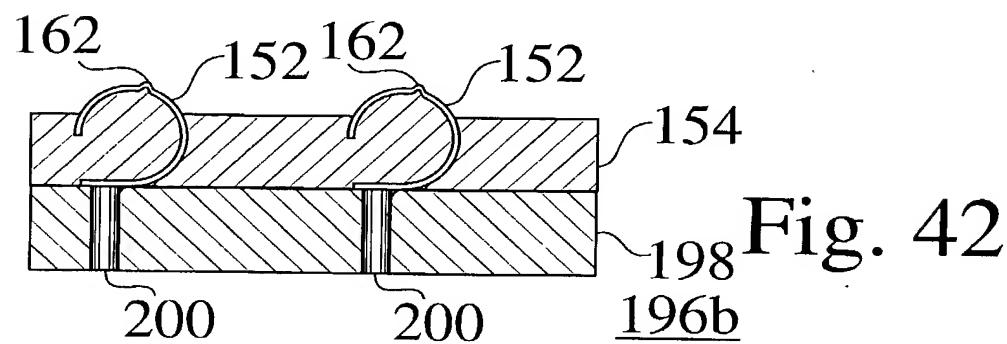
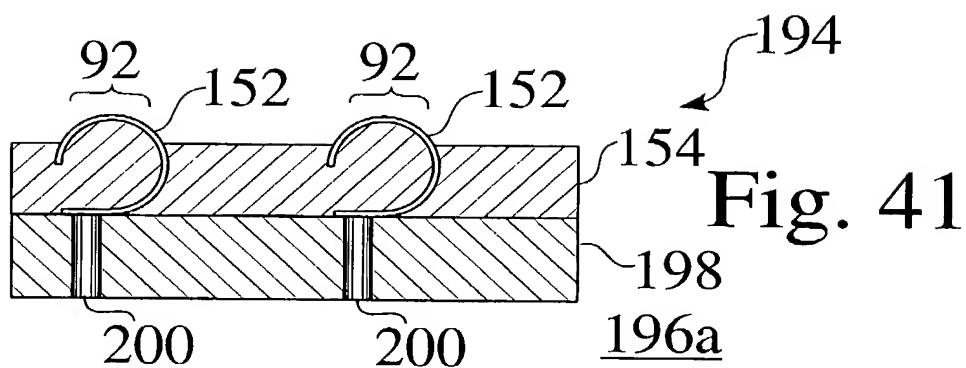
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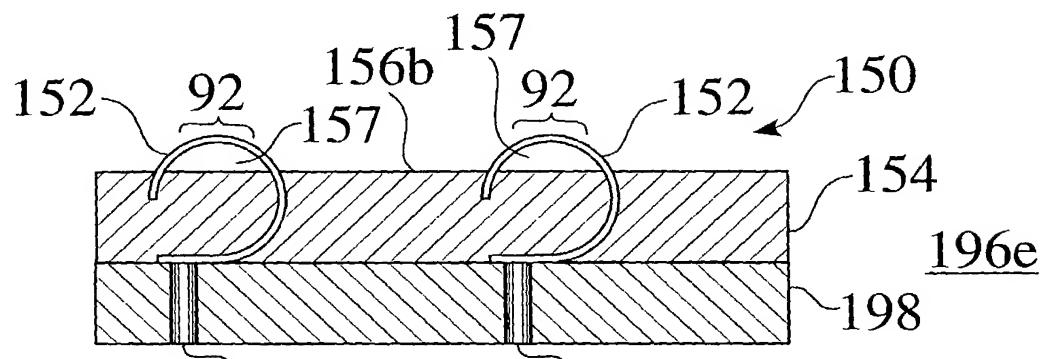


Fig. 45

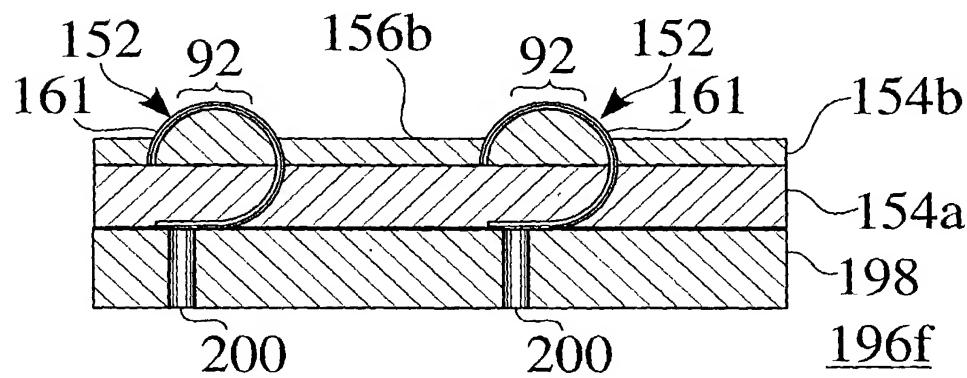


Fig. 46

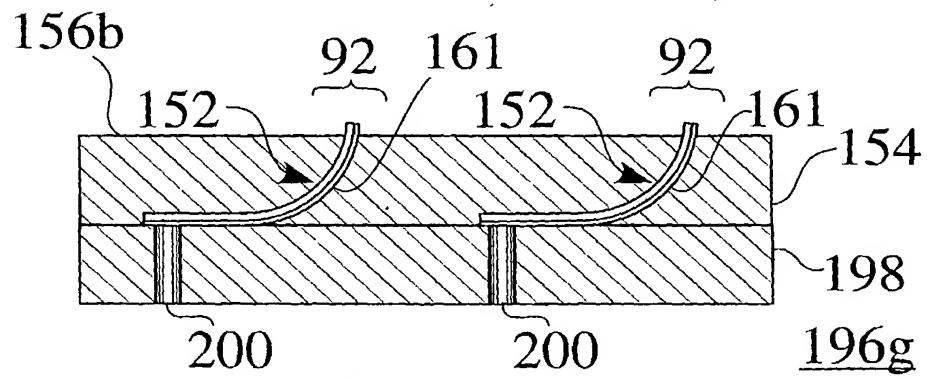


Fig. 47

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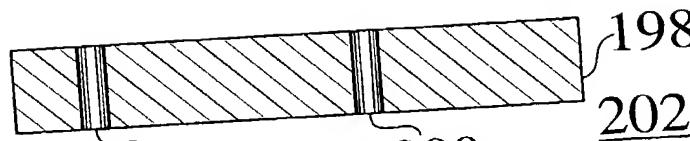


Fig. 48

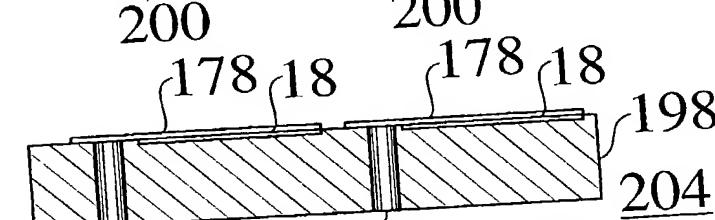


Fig. 49

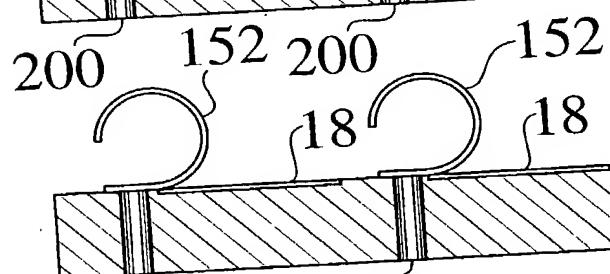


Fig. 50

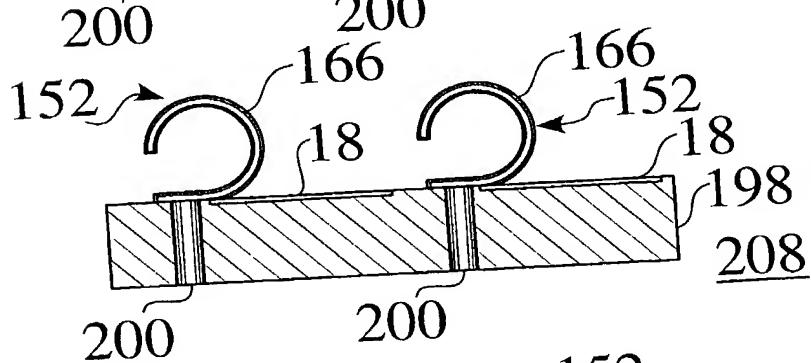


Fig. 51

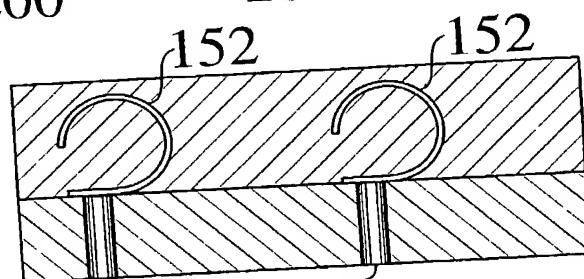


Fig. 52

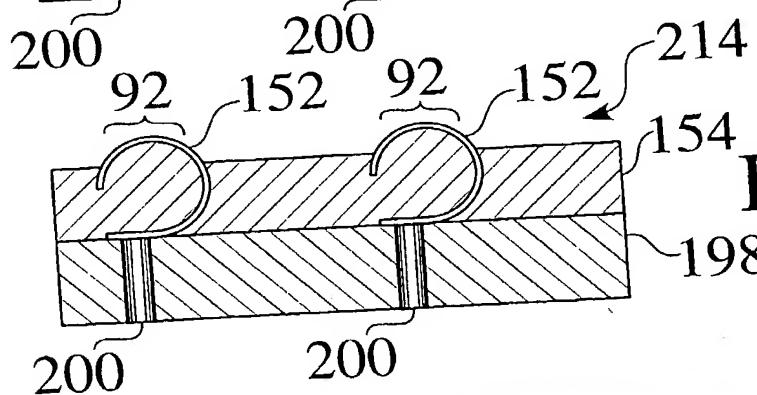
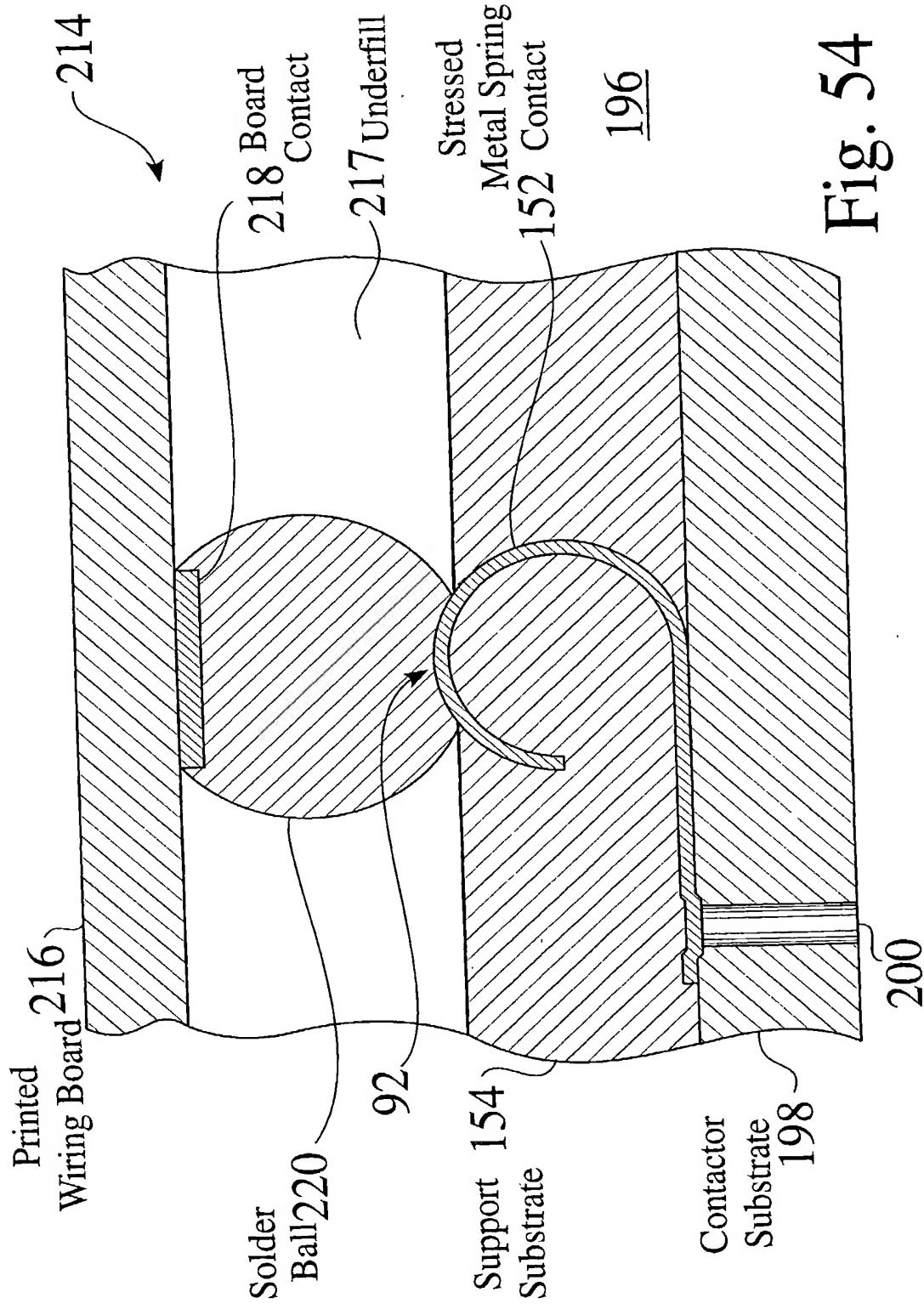


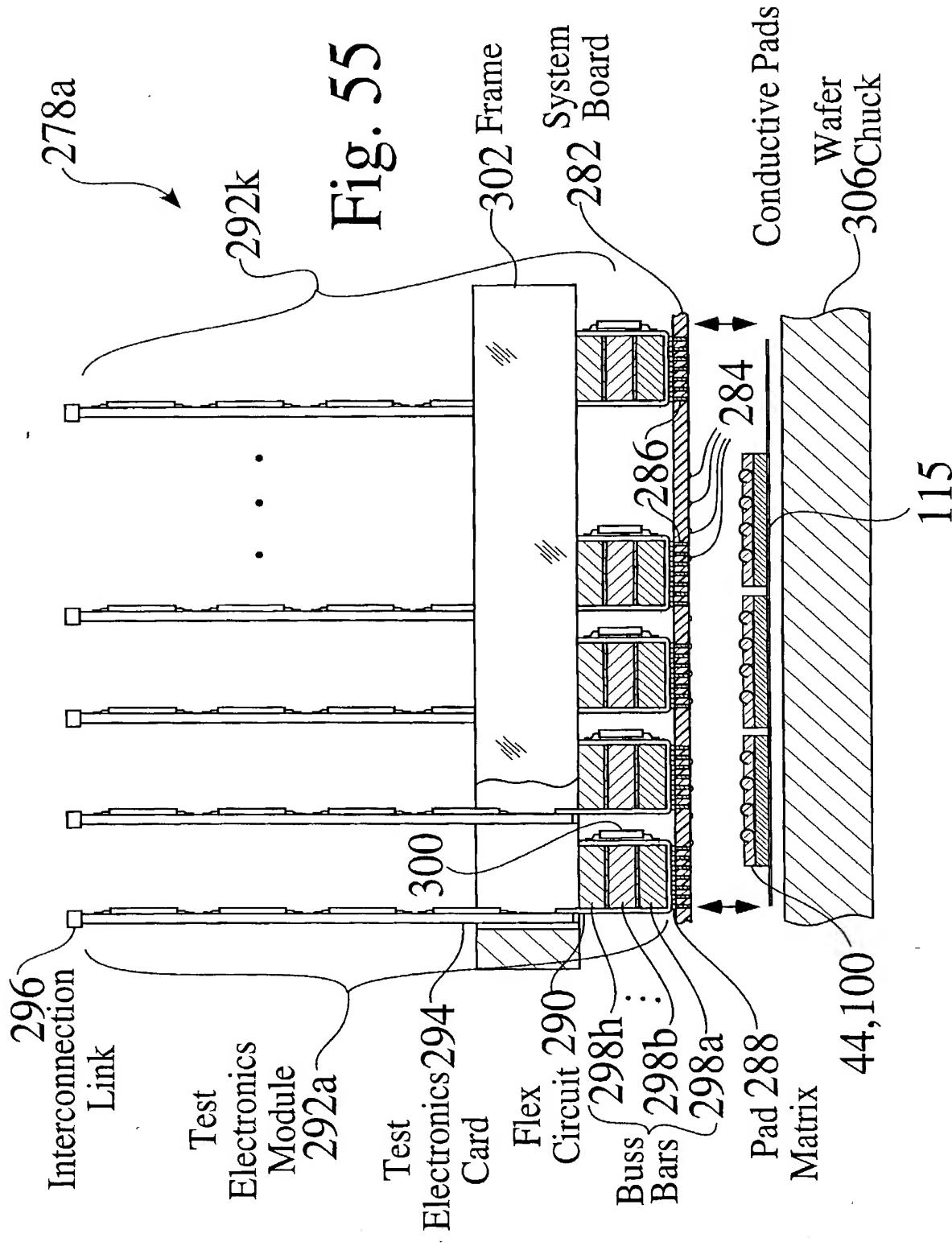
Fig. 53

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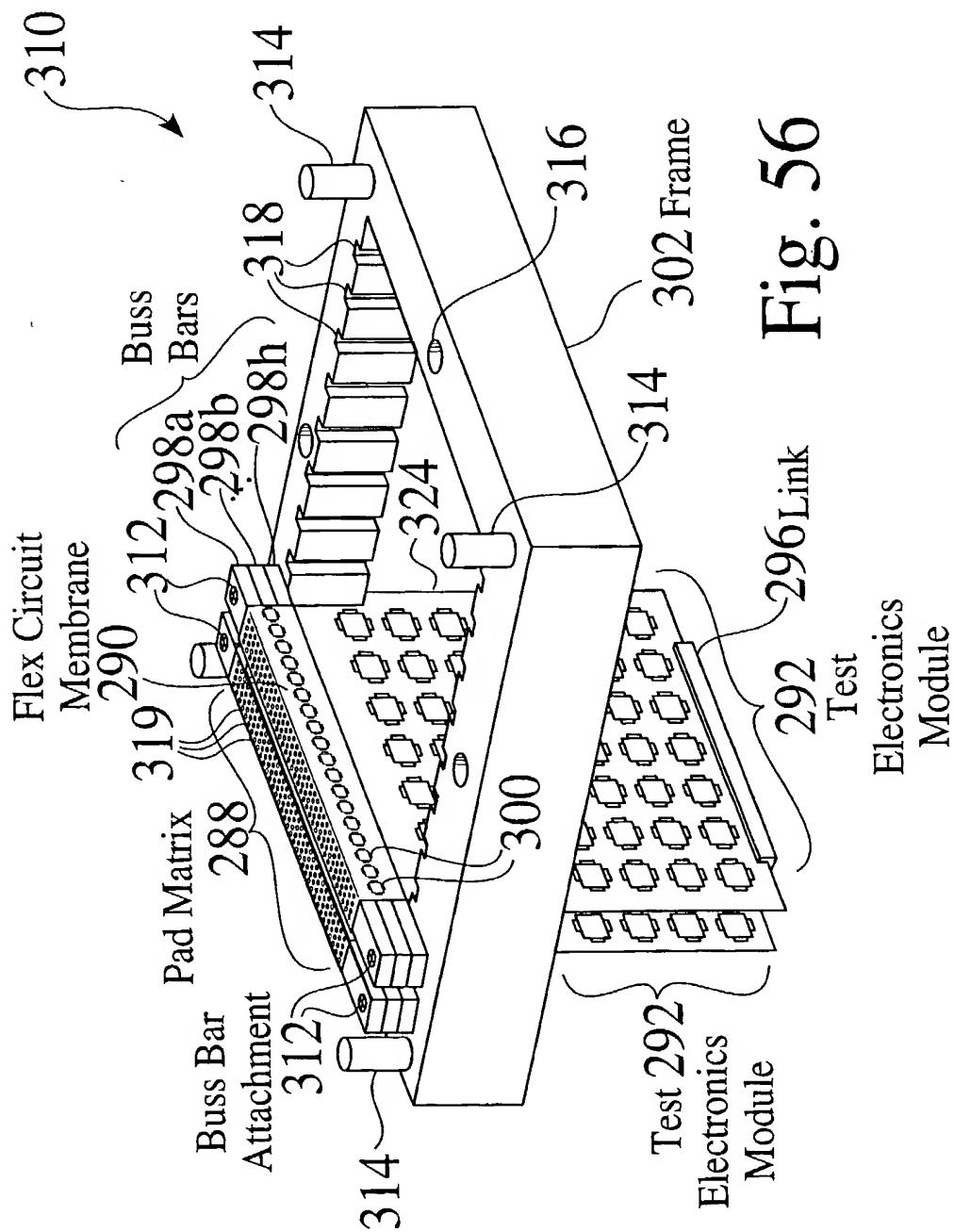


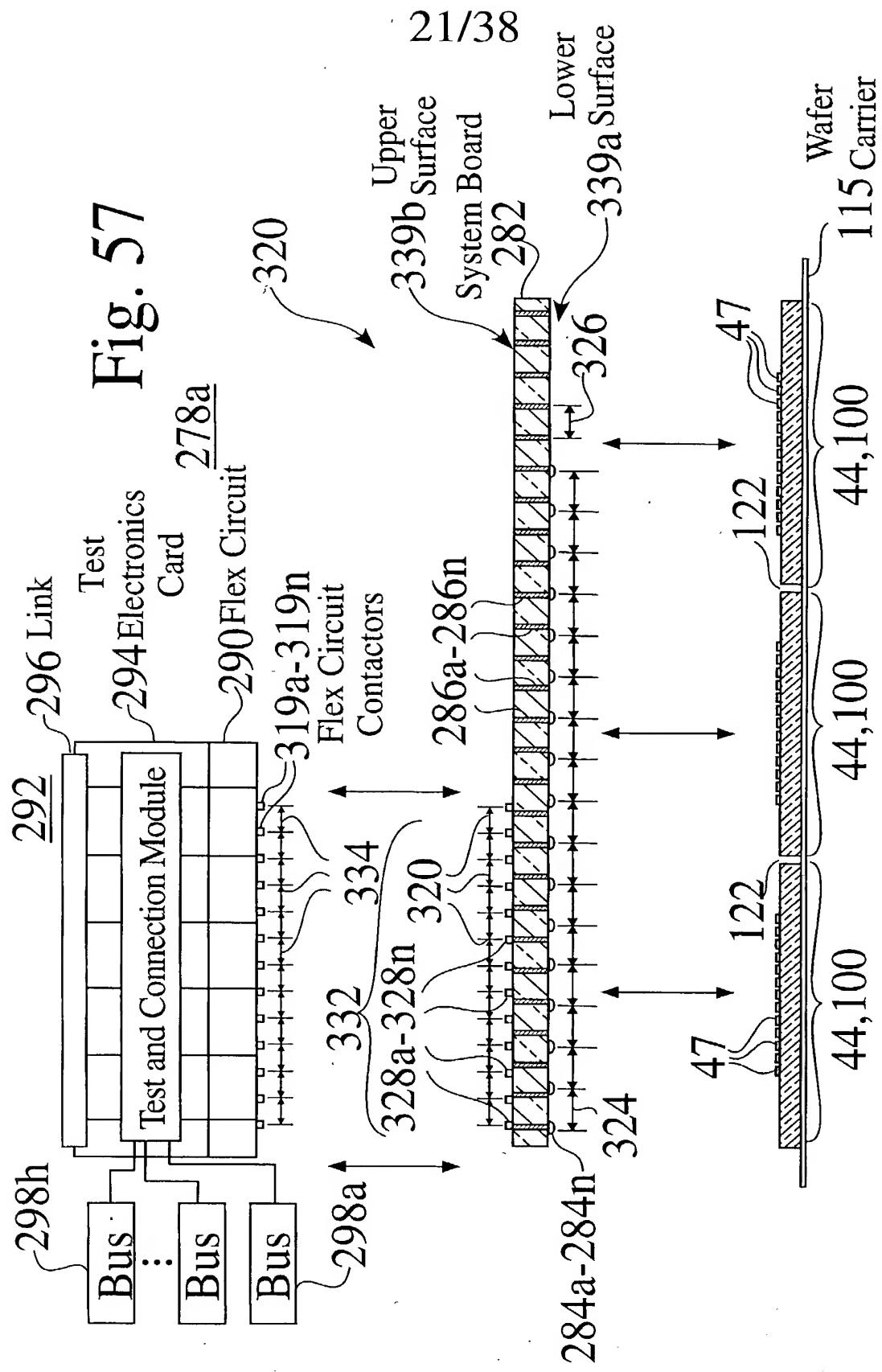
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Fig. 55



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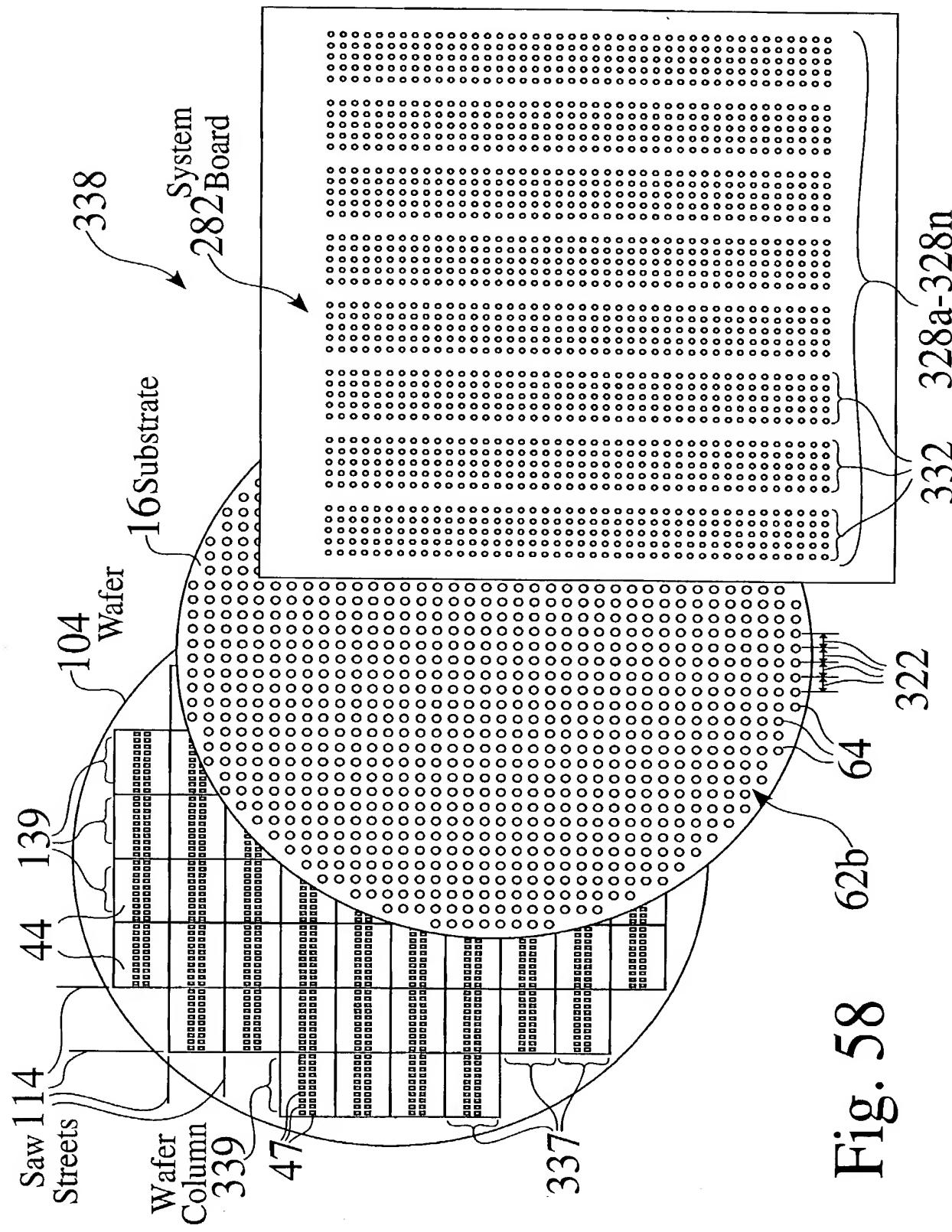


Fig. 58

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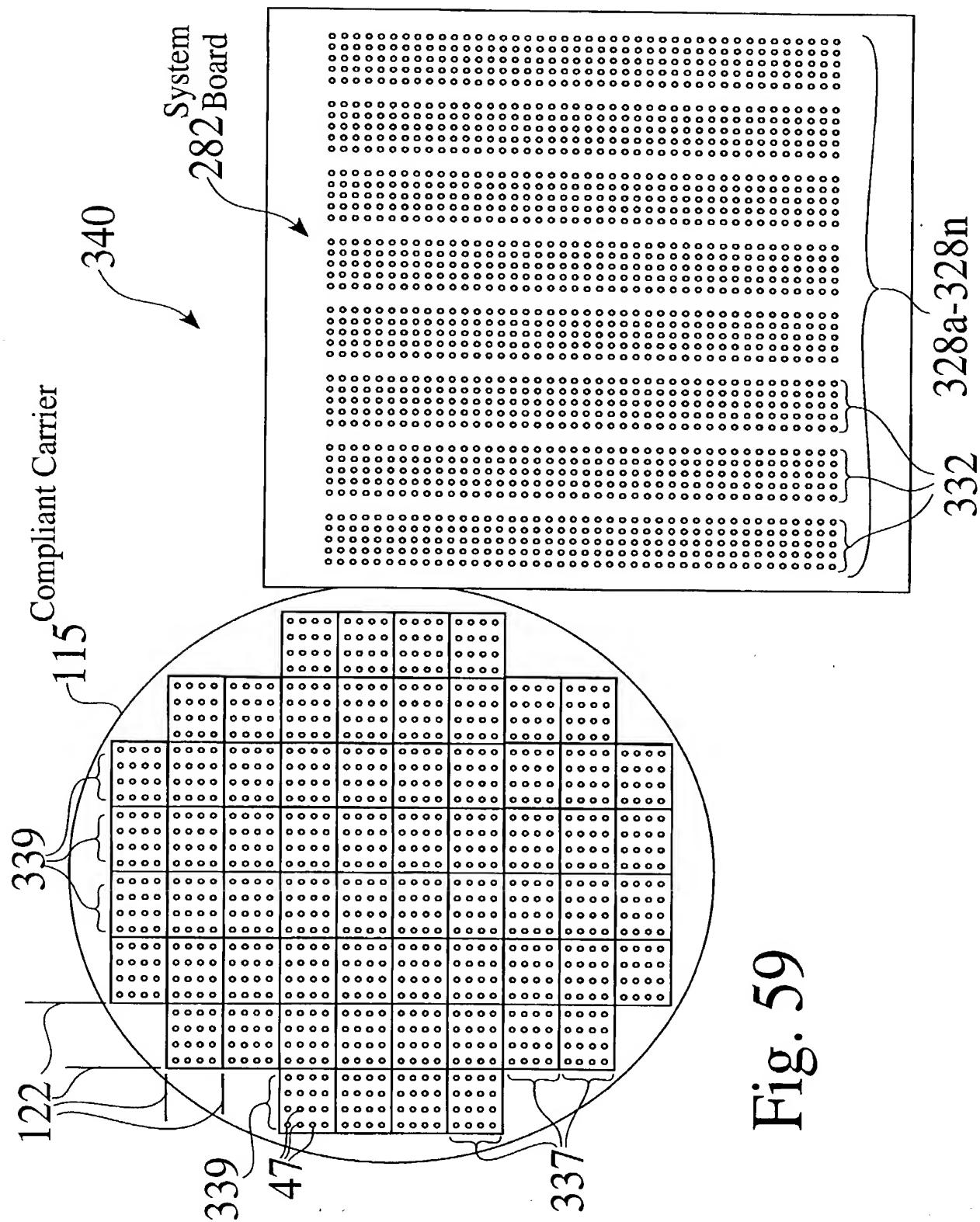


Fig. 59

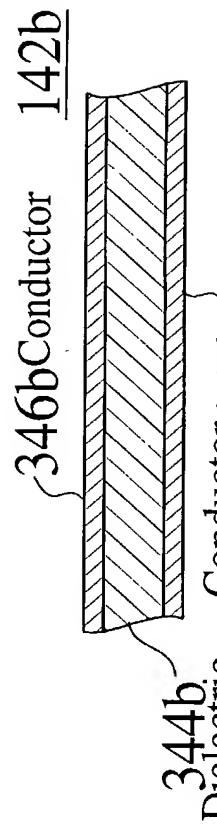
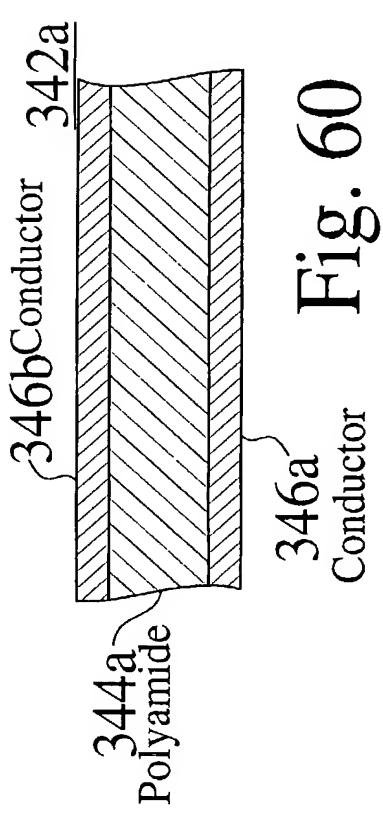
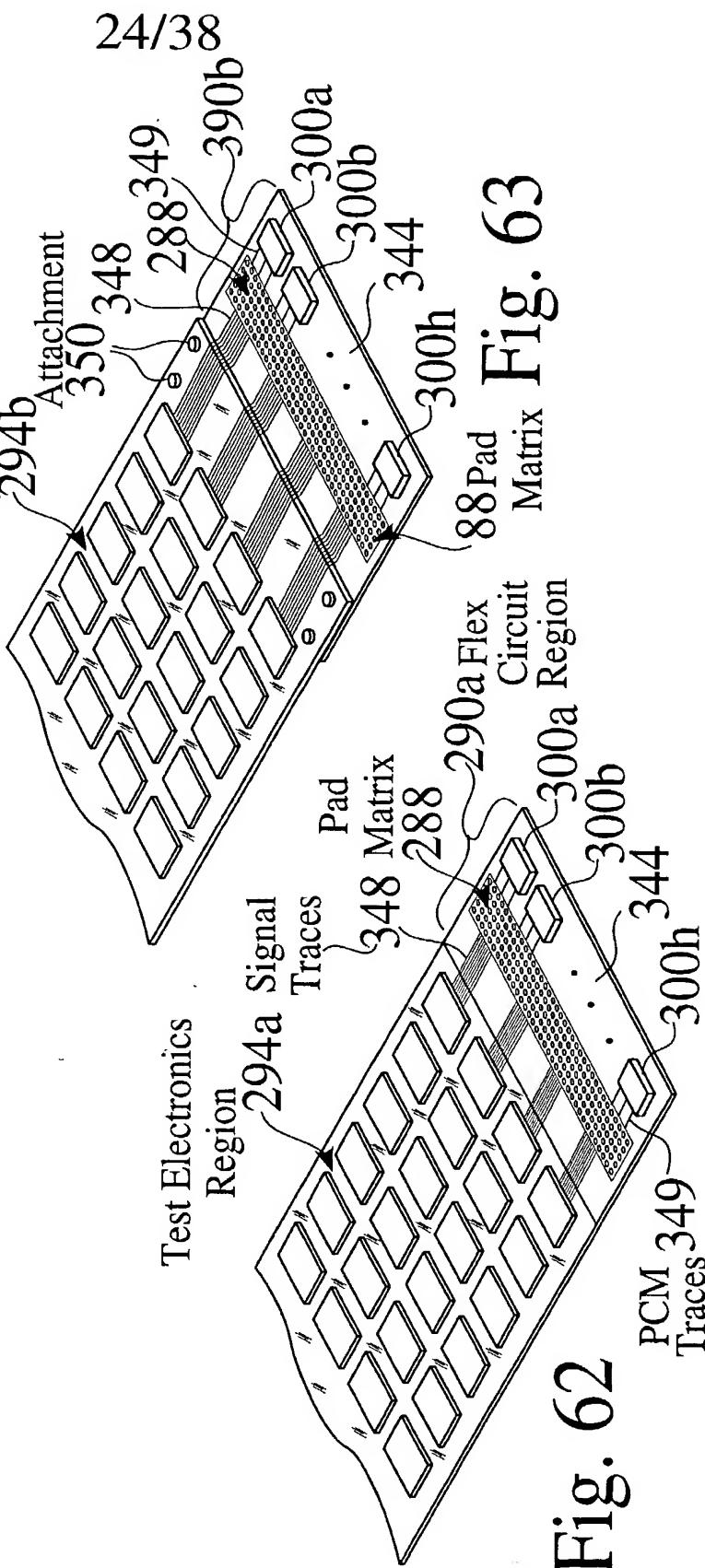
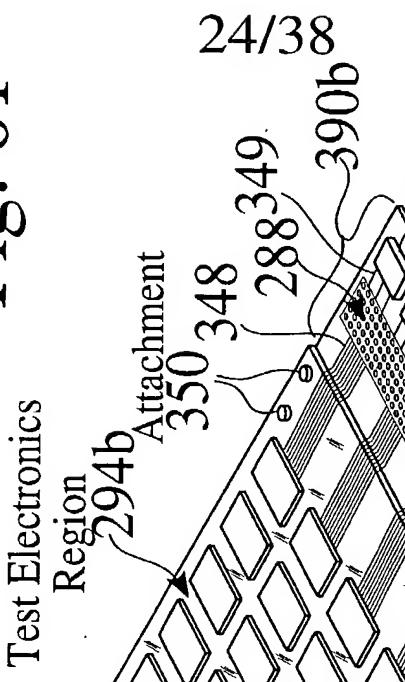
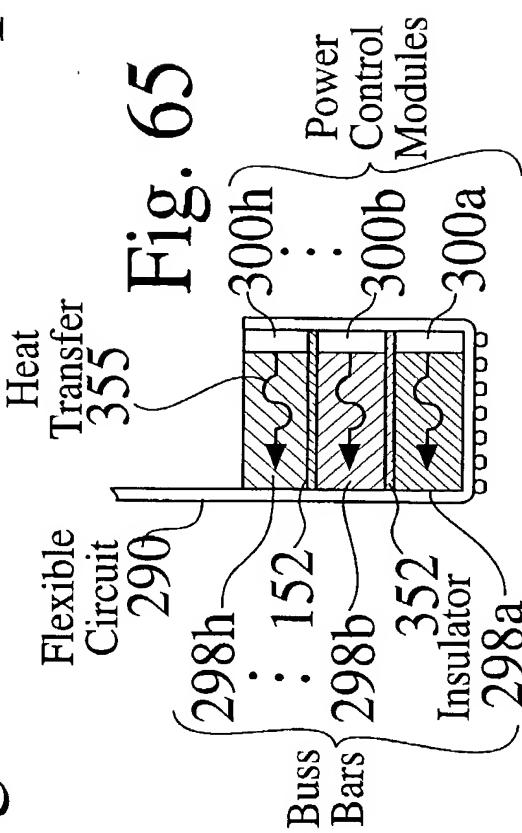
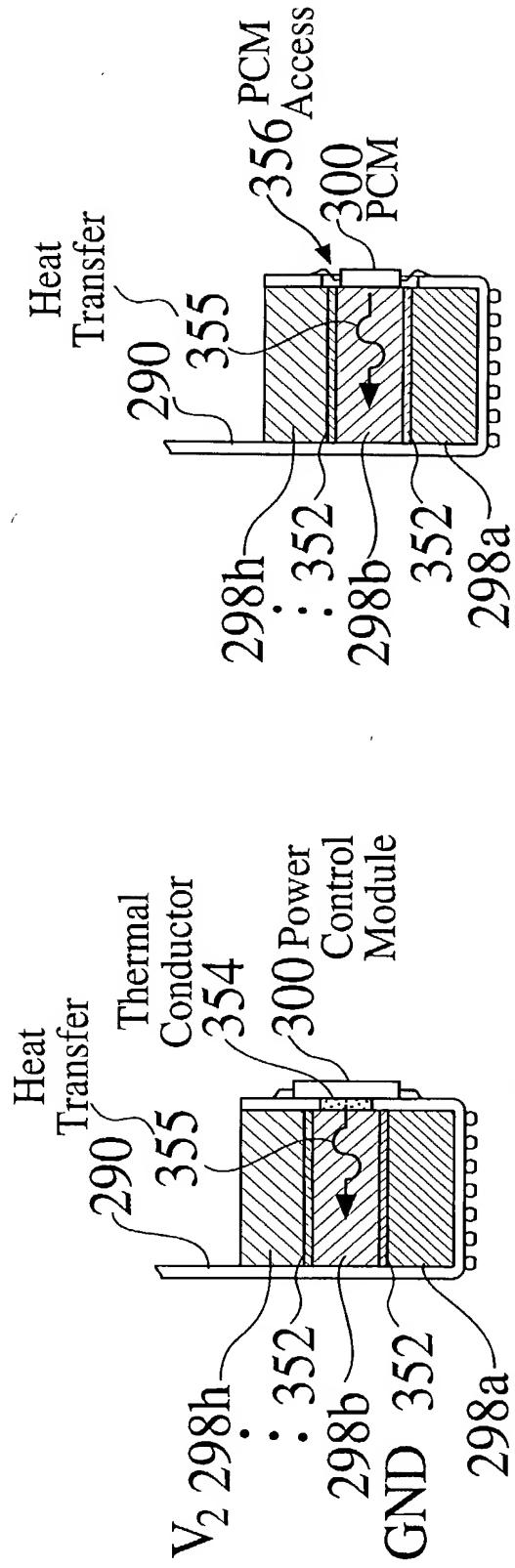


Fig. 60



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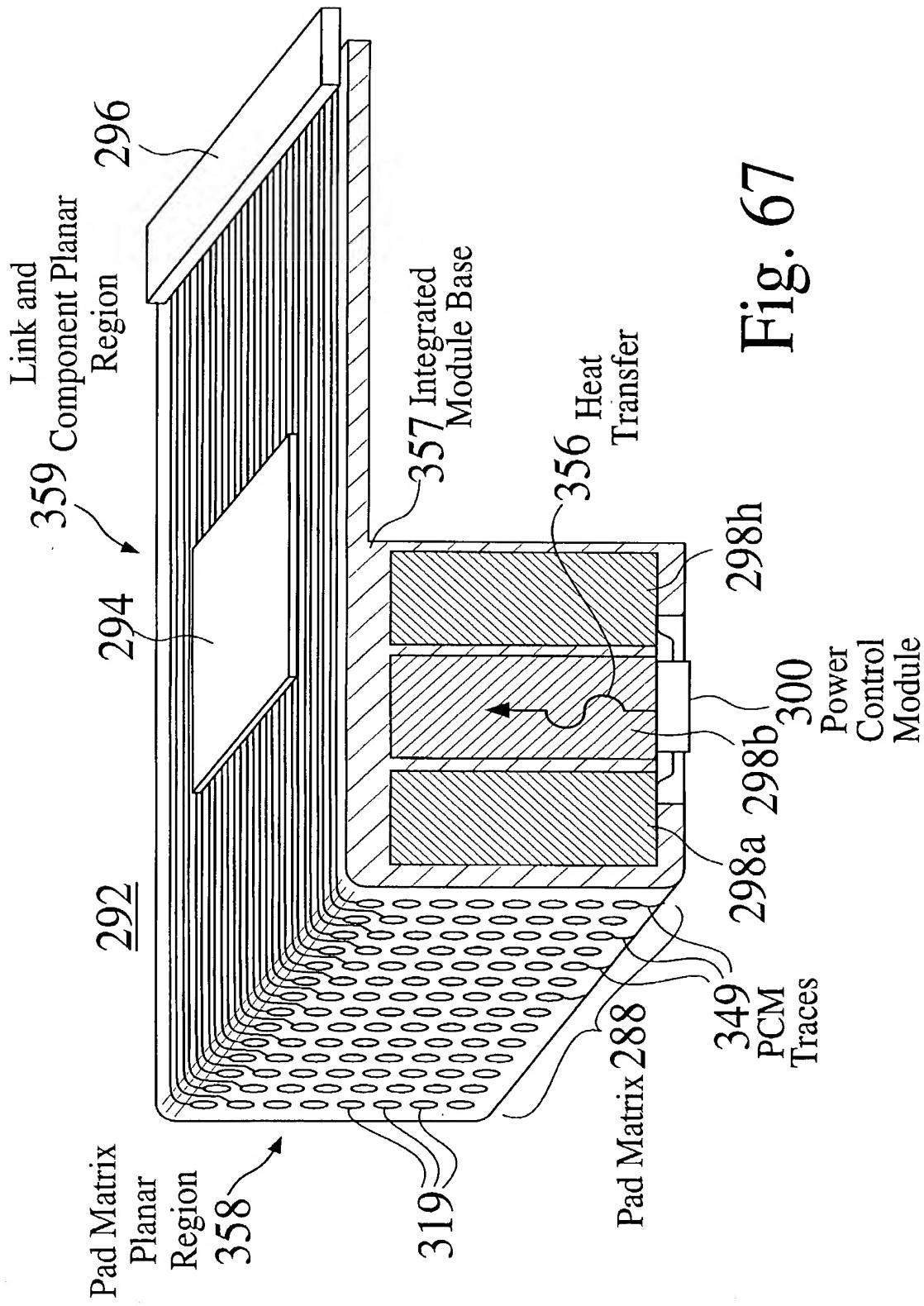
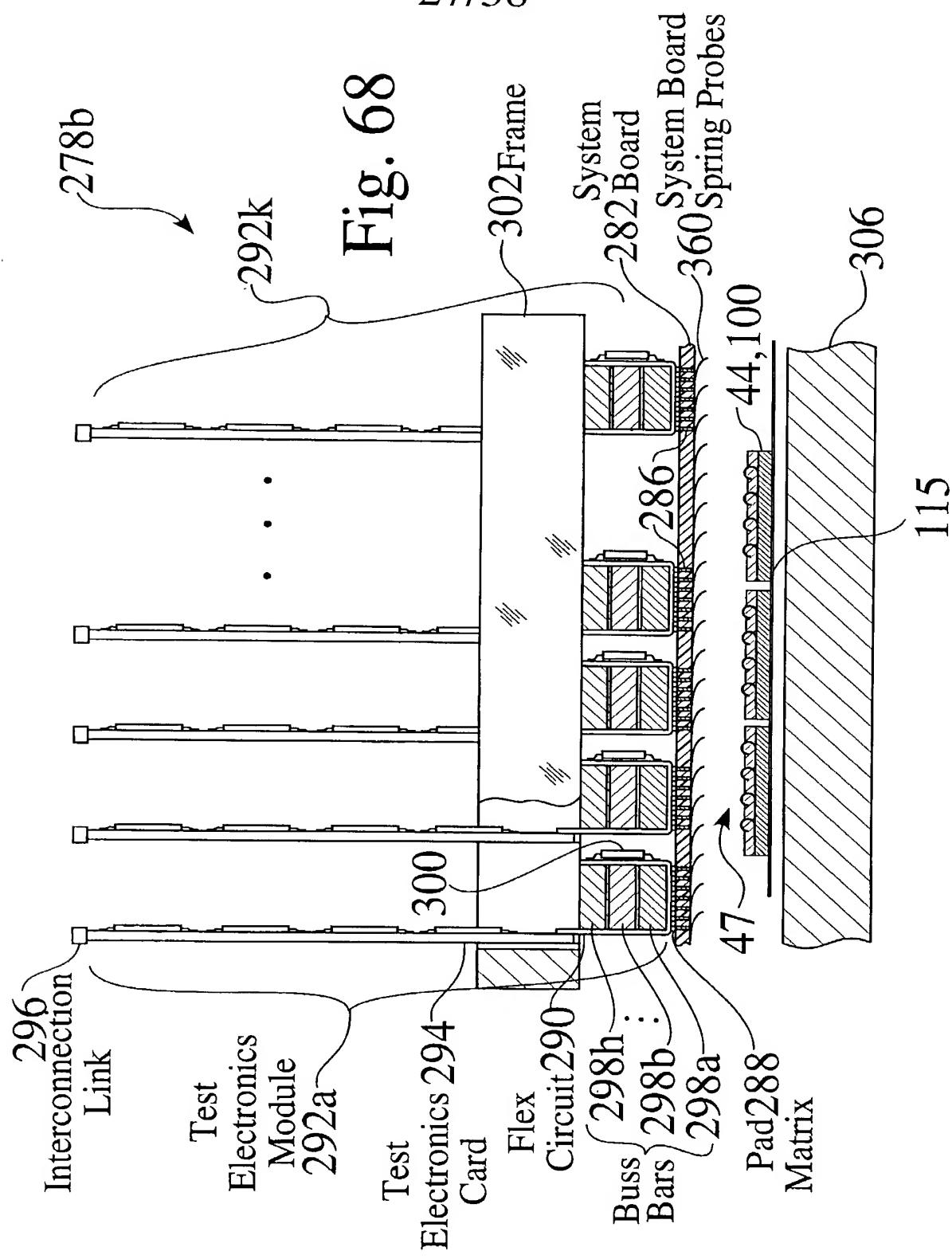


Fig. 67

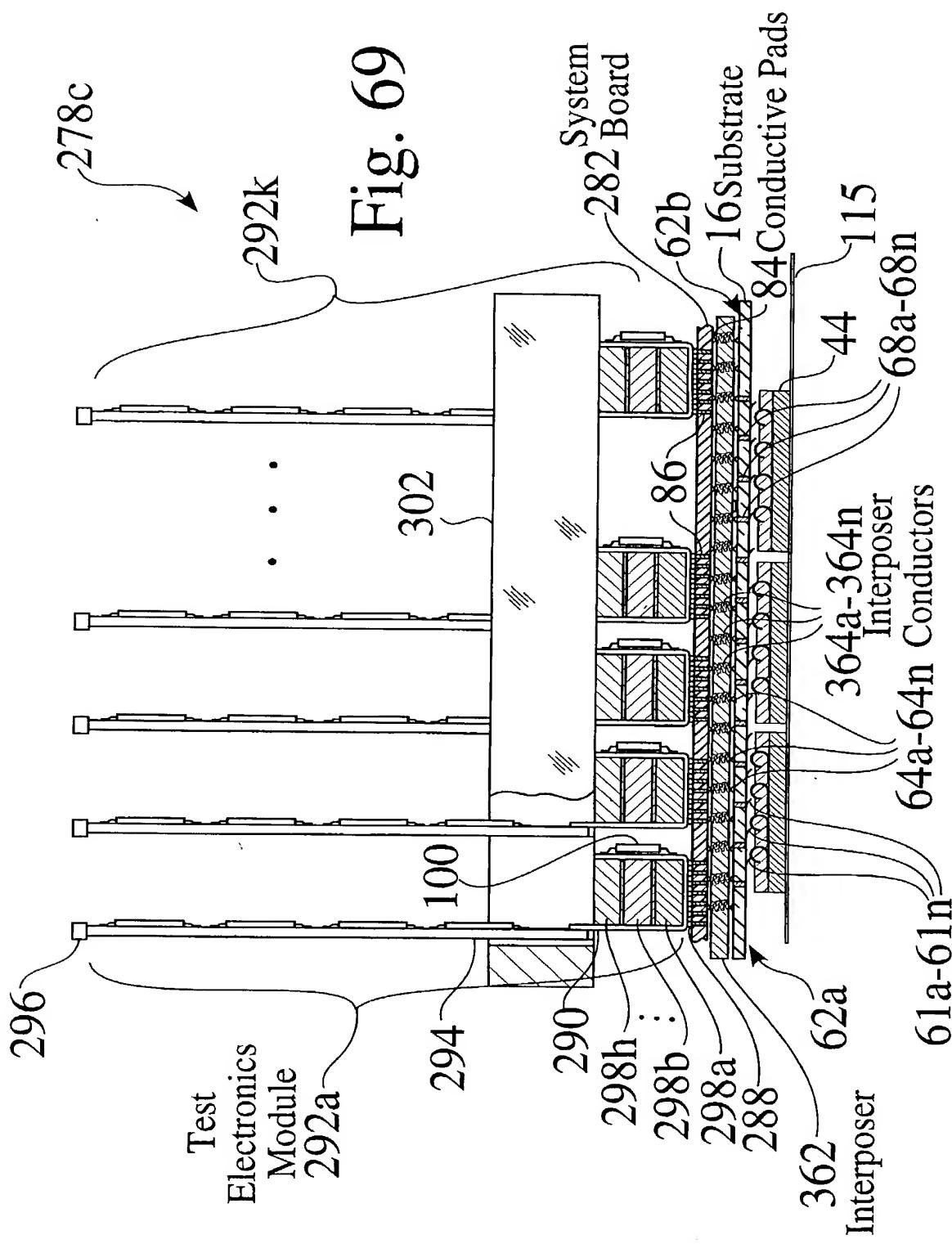
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Fig. 68

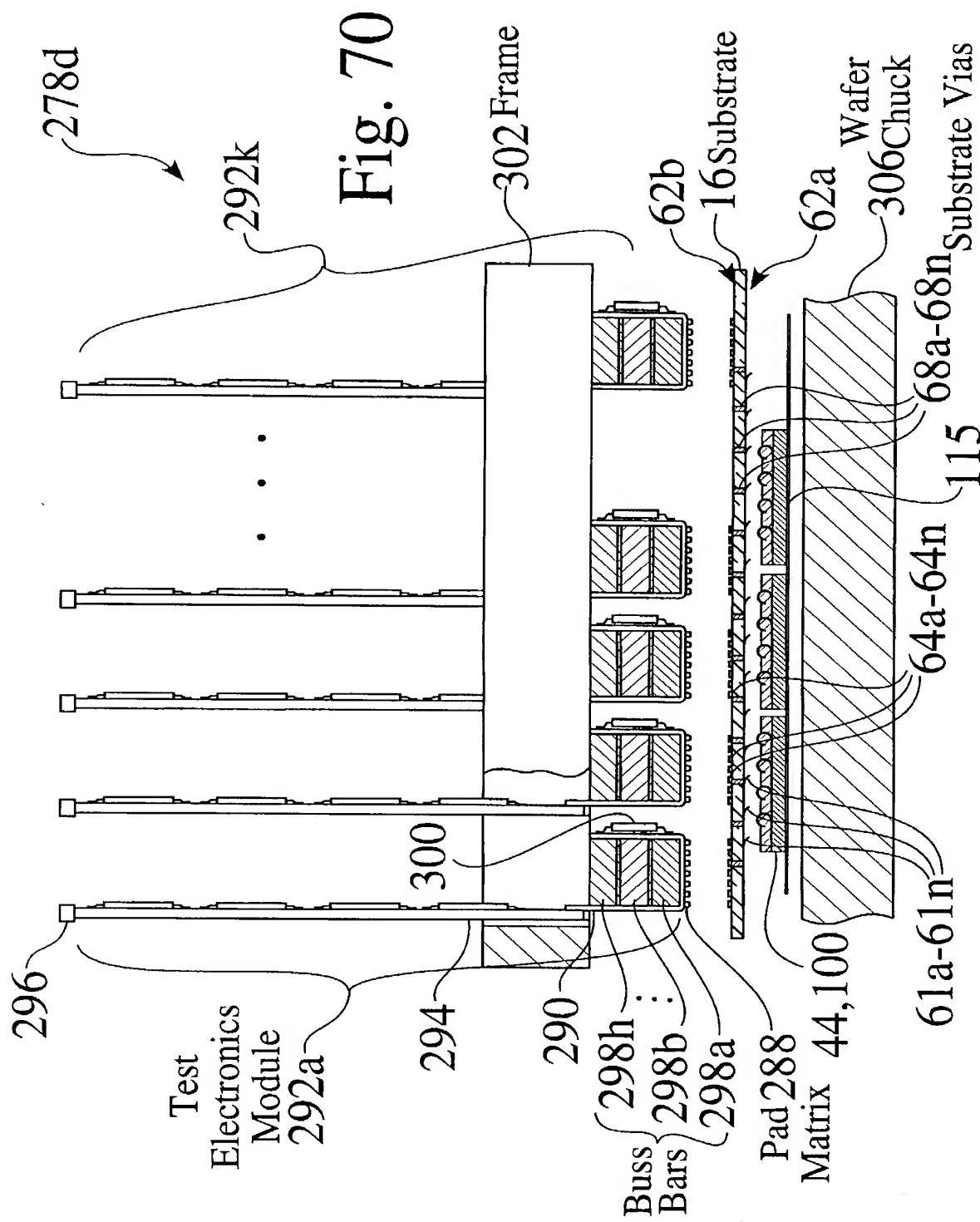


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Fig. 69



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Fig. 71

292 296 Link

294 Electronics Card

290 Flex Circuit

78d 166

298h

298a

334

368

64a-64n 322

68a-68n Substrate

75 58 20

75 15 61a-61n 20

75 74a,74b

Bus

Bus

Bus

319a-319n Flex Circuit Contactors

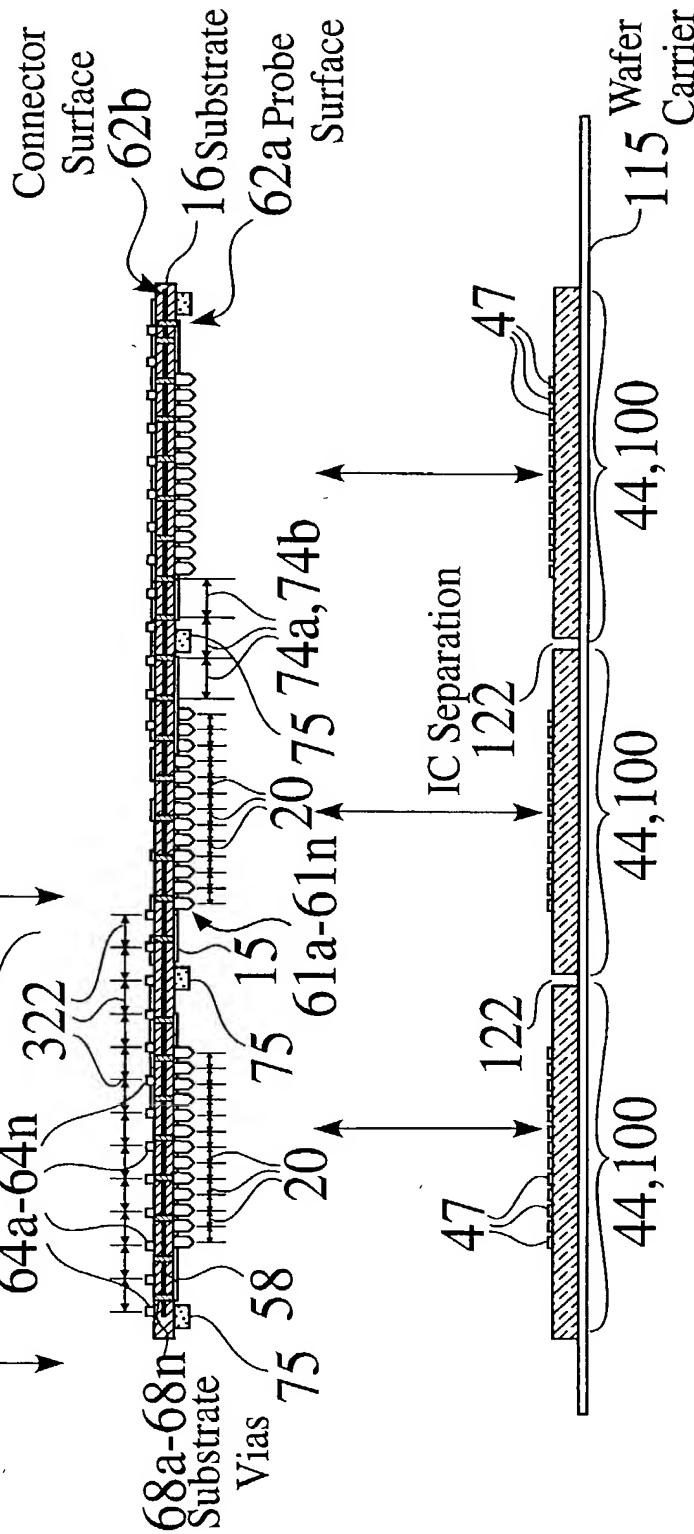
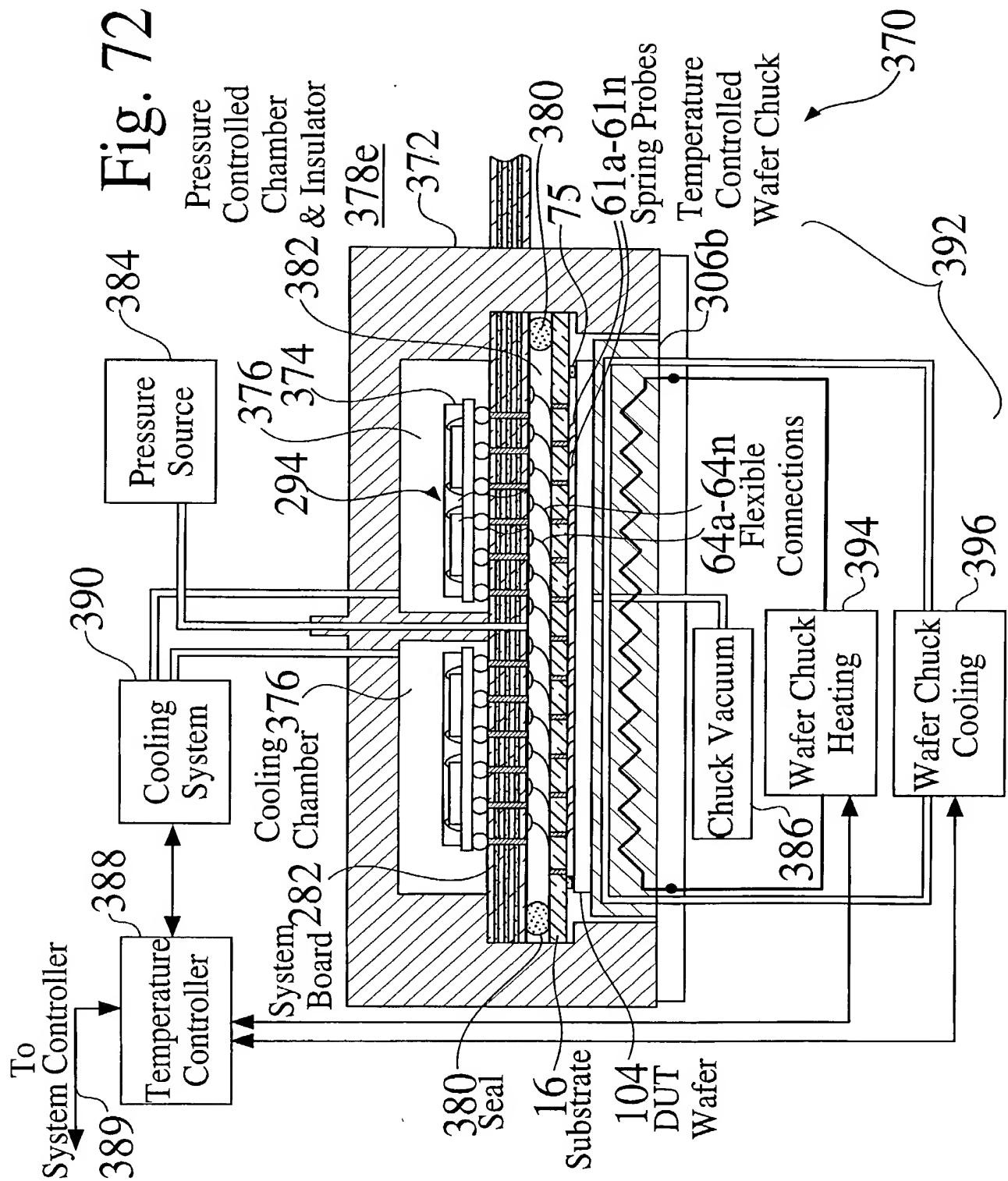


Fig. 72



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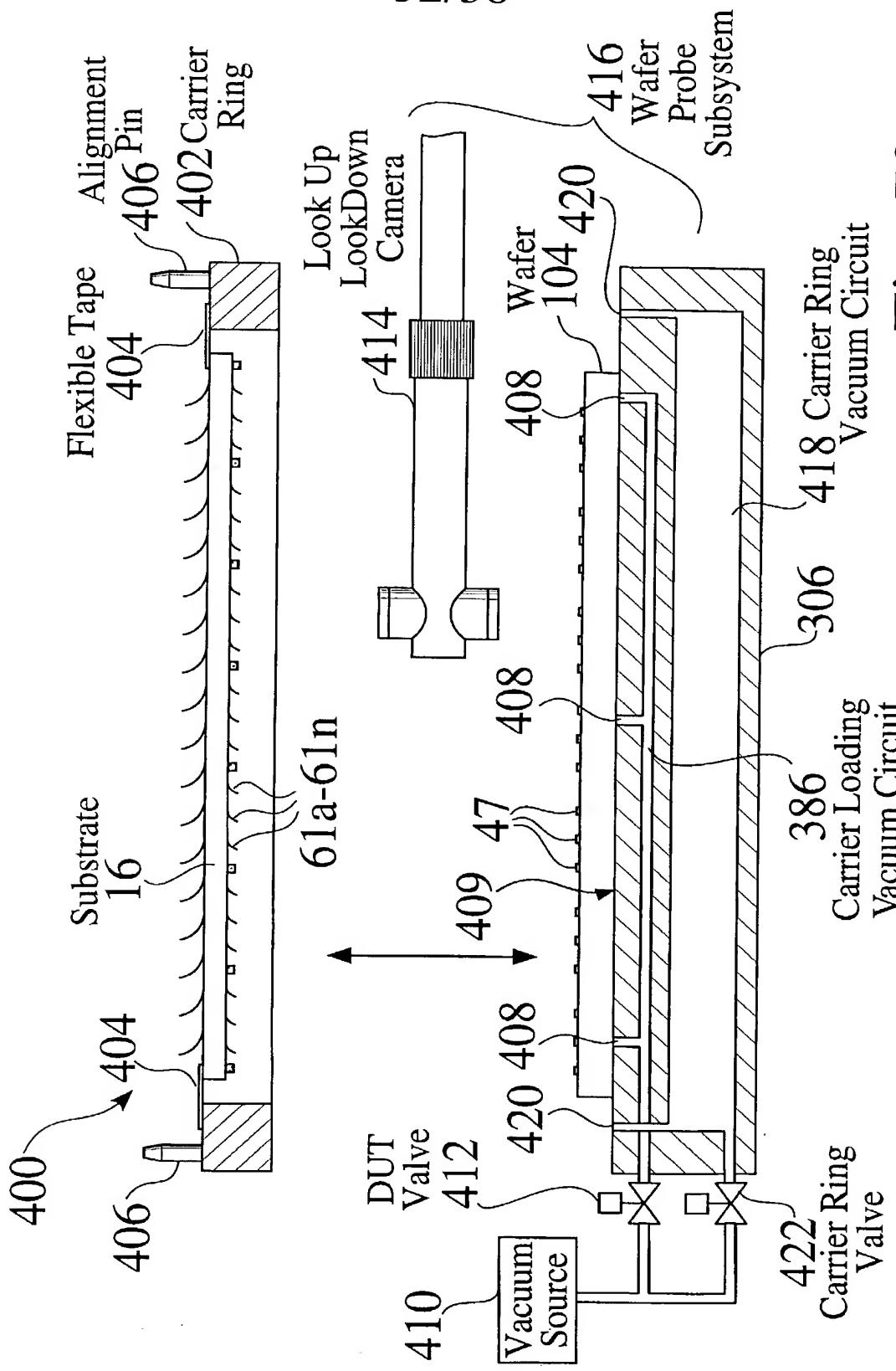


Fig. 73

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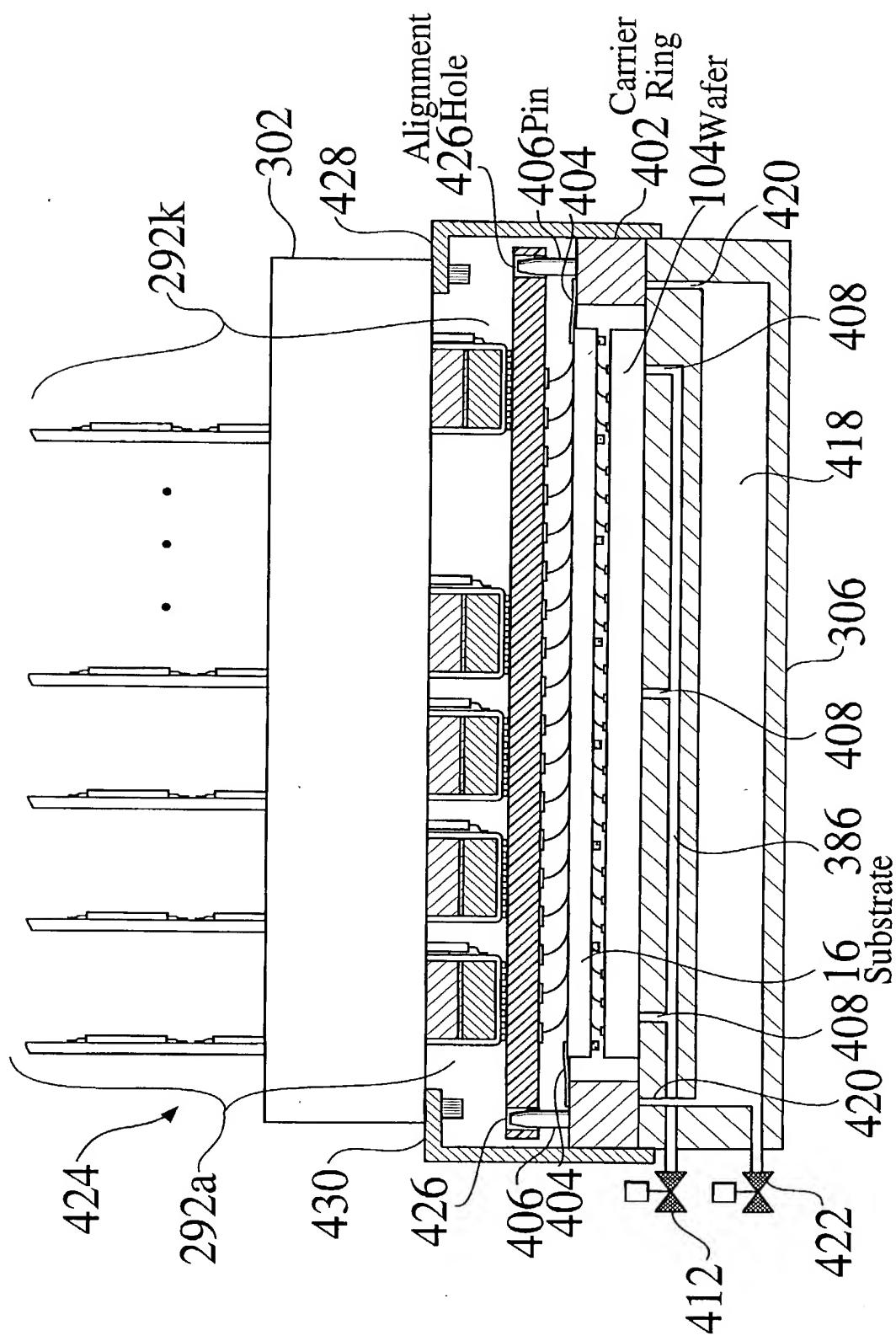
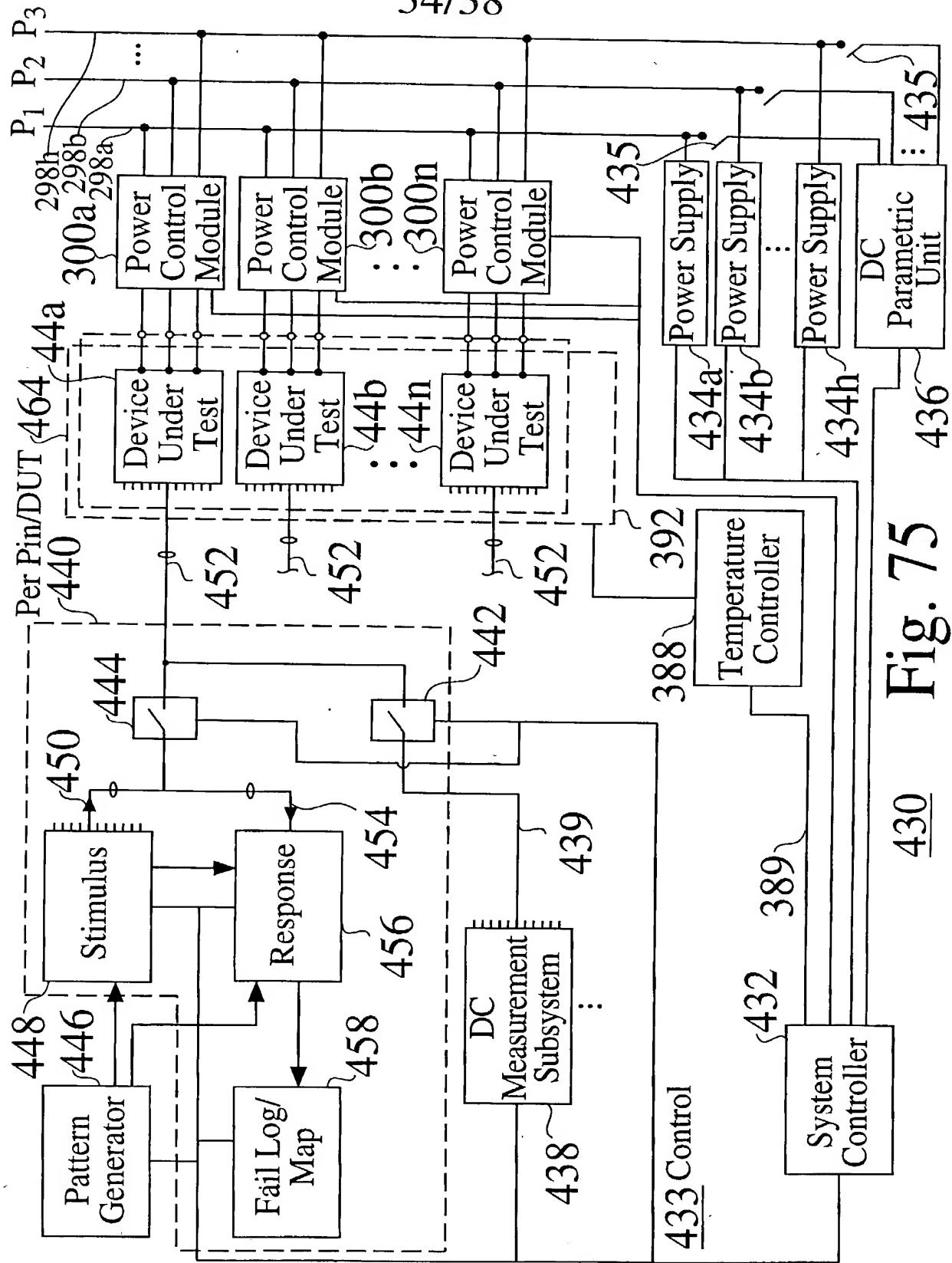


Fig. 74

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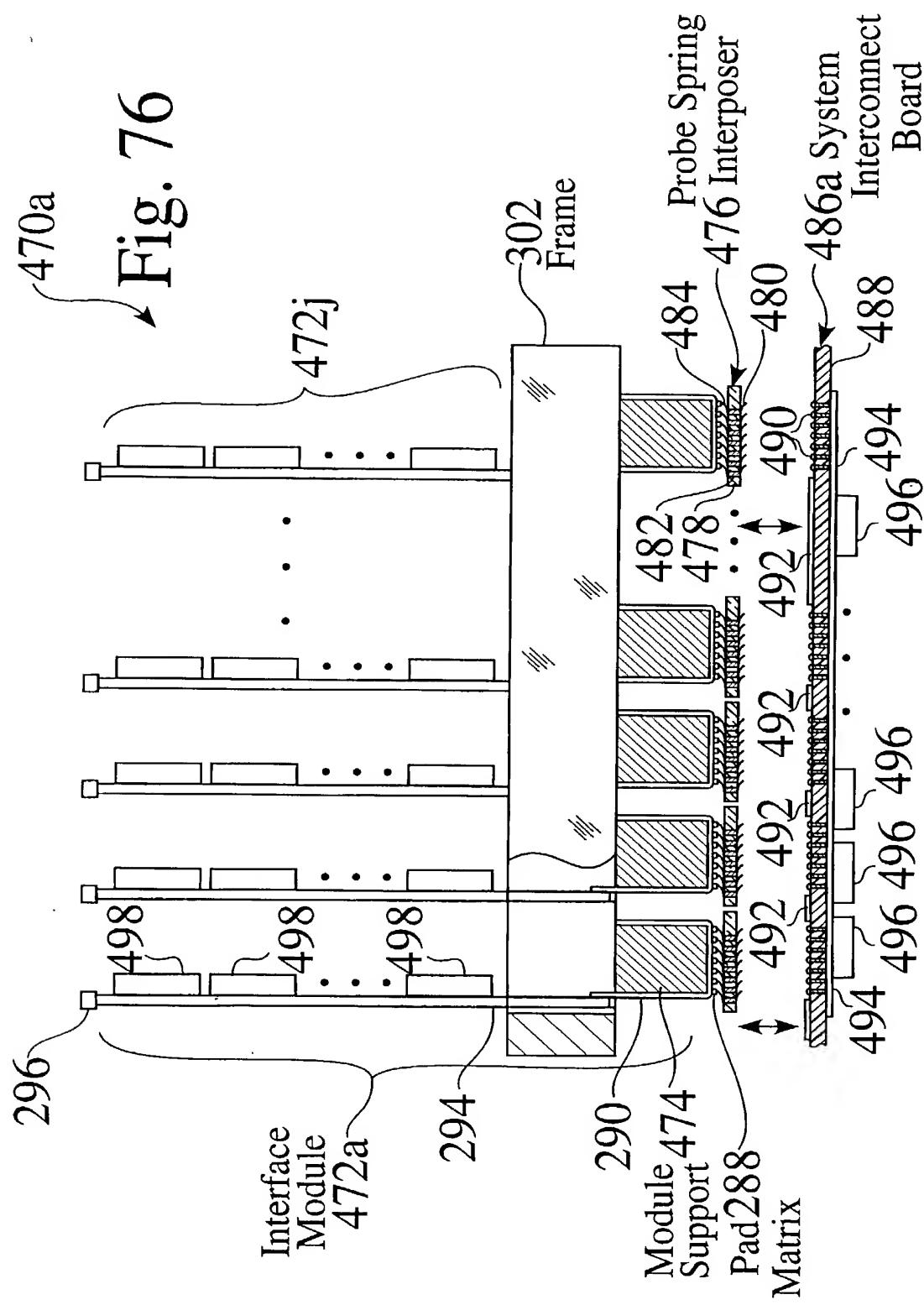
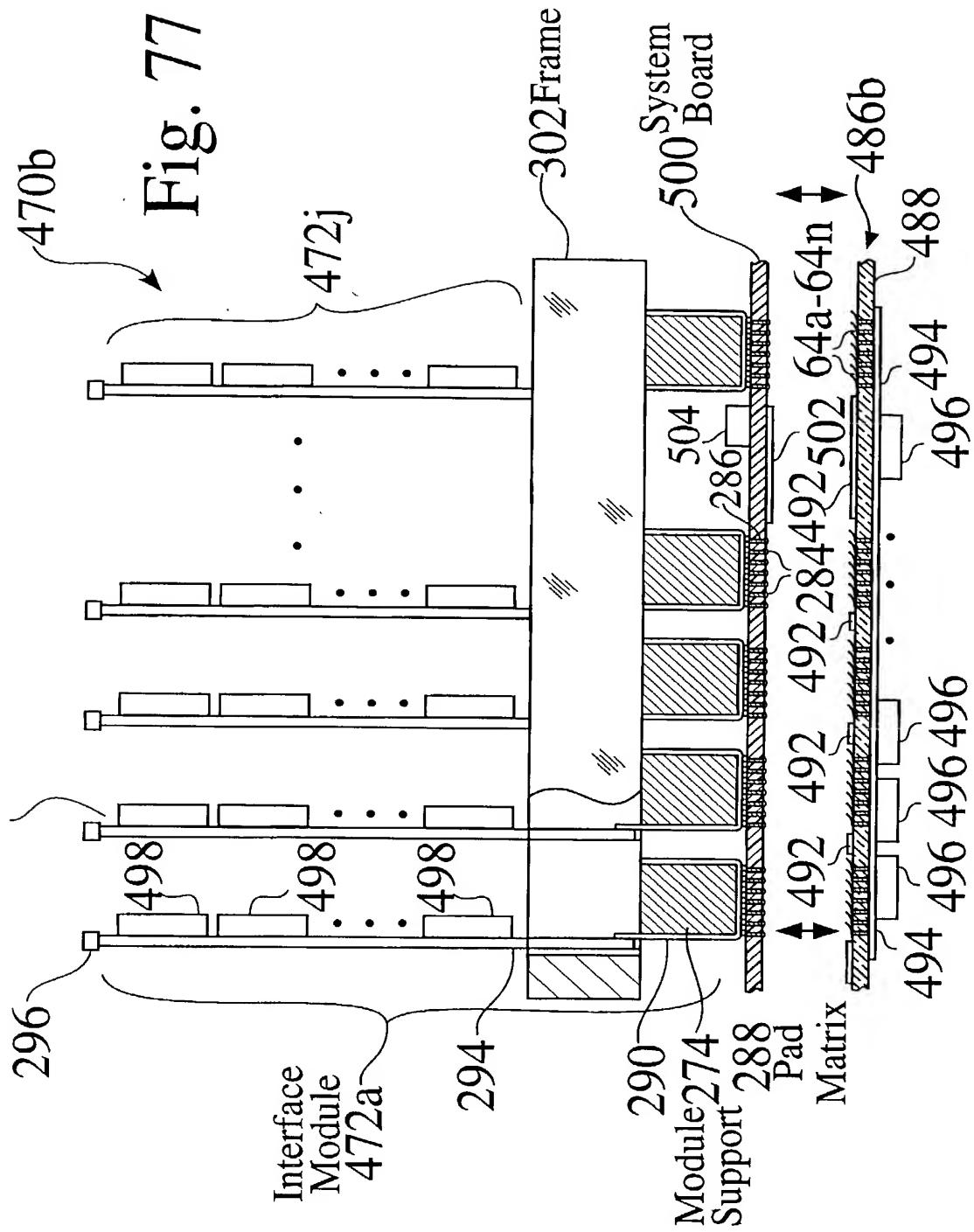


Fig. 77



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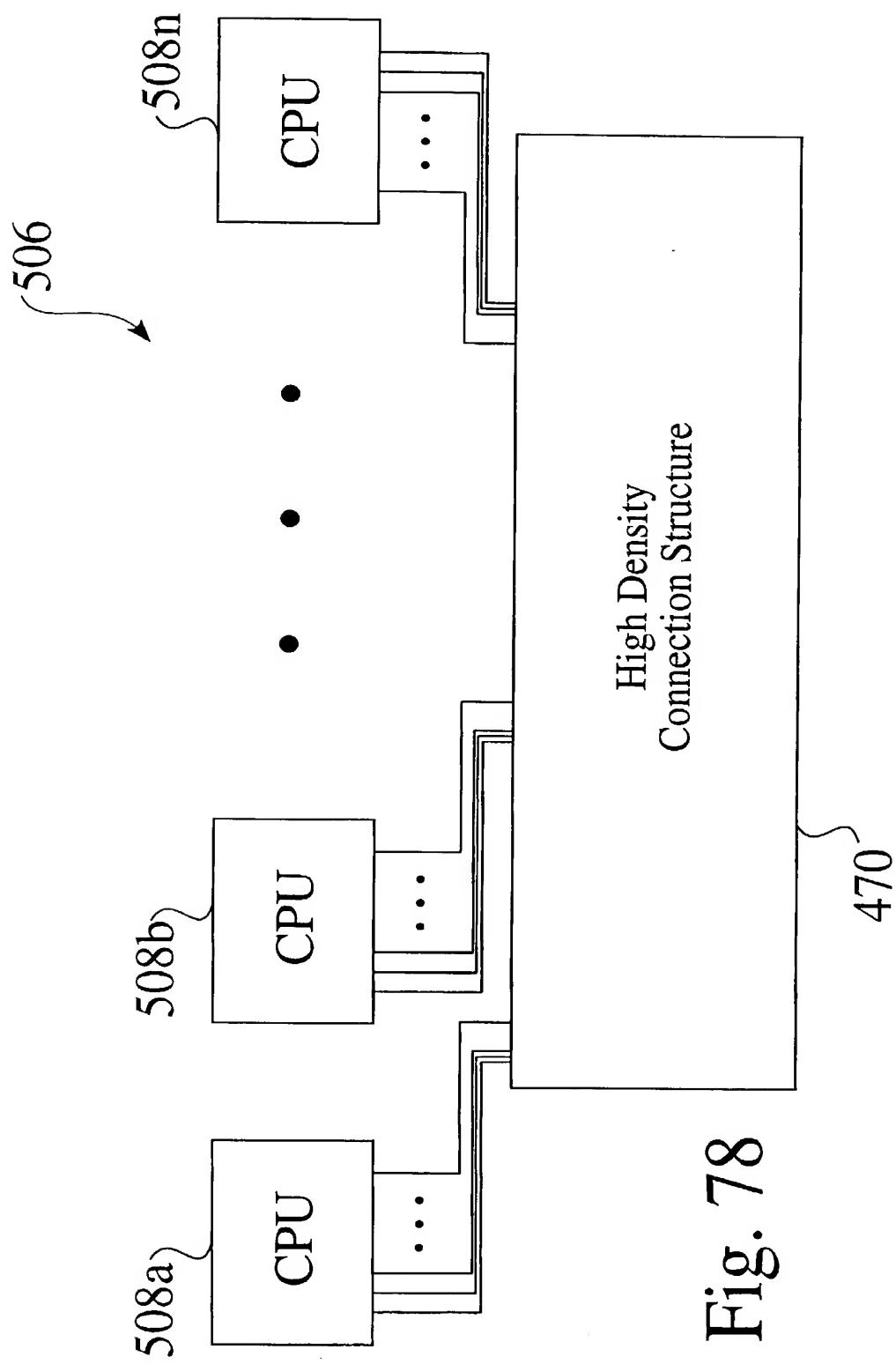


Fig. 78

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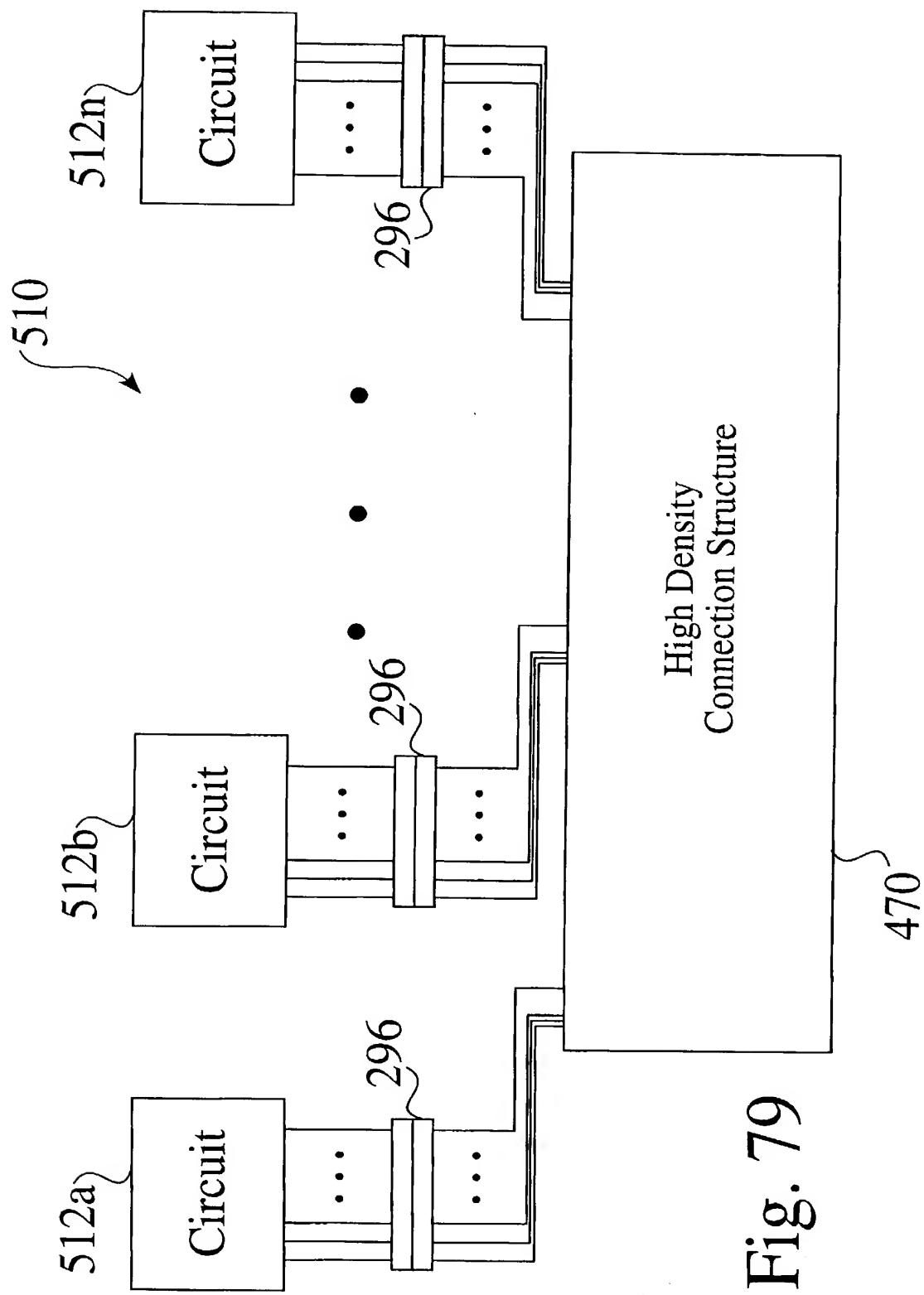


Fig. 79